## New Distribution Paradigms for Railway Interlocking

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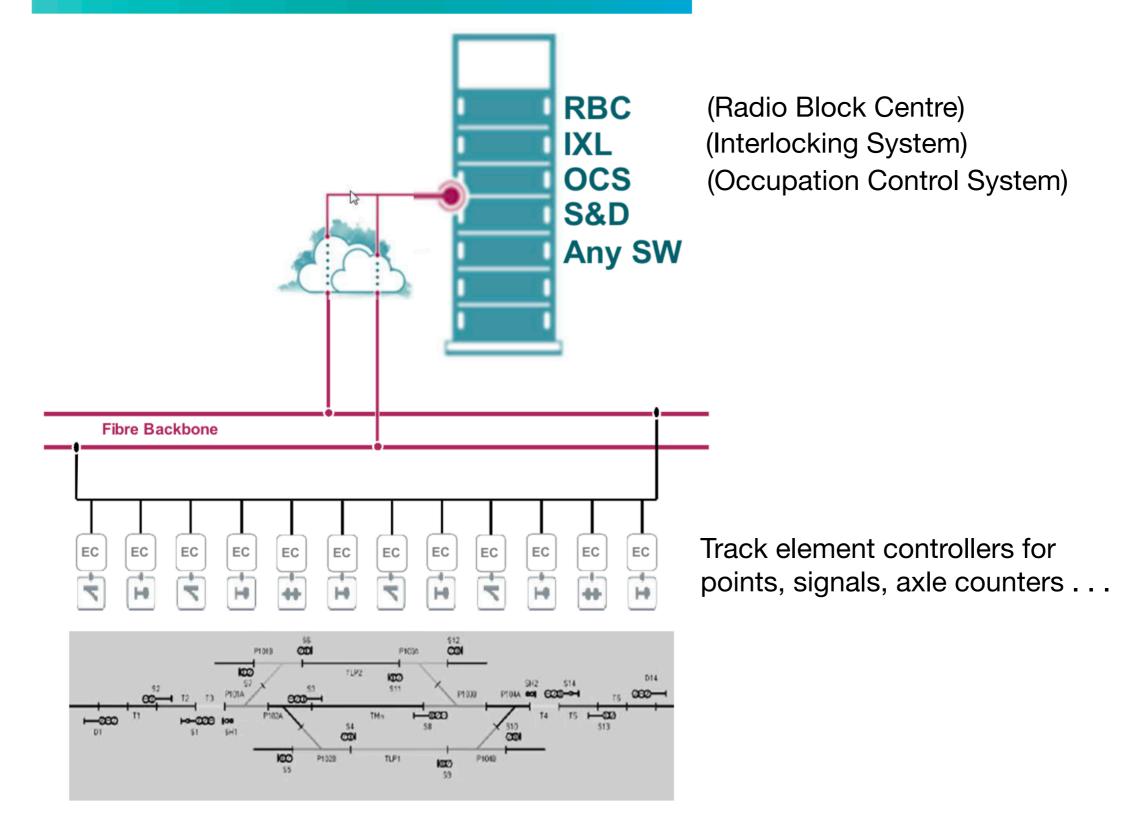


### A Novel Distribution Paradigm. Cloud-based Railway Control

### Cloud-based Railway Control

- Siemens Mobility **DS<sup>3</sup> Distributed Smart Safe System** 
  - IXL, RBC and related functionality are moved into the cloud
  - Functions run safely on standard HW, standard OS, and standard VMs
  - Cloud severs communicate with track element controllers via high-speed back bone and Ethernet
  - see Siemens Mobility publication [1]

### Safety @ COTS multicore



Source: see [1]

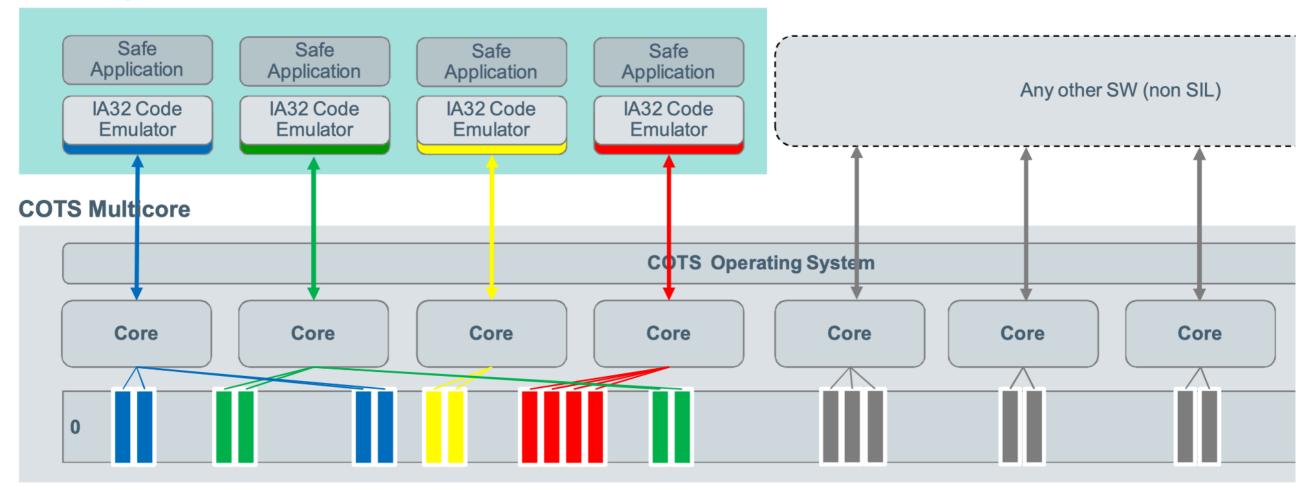
### Motivation for this Architecture

- Excellent scalability
- Excellent performance through state-of-the-art servers and networks
- Significant availability improvements enabled by
  - Reconfigurable software allocation on different CPU cores and servers
  - Geographic distribution
- Cost reduction enabled by
  - COTS operating systems and virtual machines
  - COTS hardware virtualisation removes HW dependencies
  - Mixed SIL runnable on the same HW
  - Legacy software running in emulators on high-performance COTS servers

# Challenges

- Ensure fail-safe behaviour on unsafe HW, OS, VM
- Safe synchronisation between geographically distributed components
- Safe reconfiguration during system operation

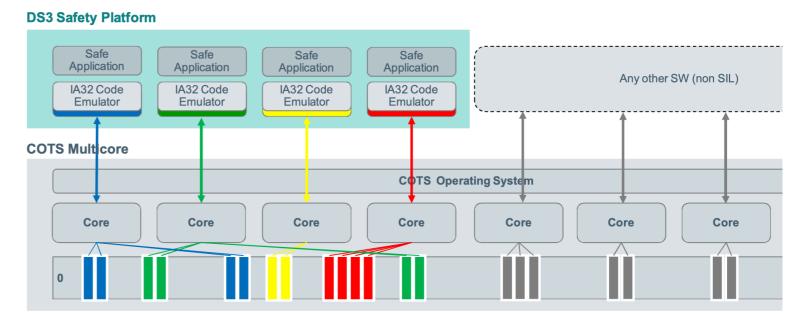
#### **DS3 Safety Platform**



Source: see [1]

Create fail-safe behaviour using principles of the coded monoprocessor

A specific approach to **software diversity** 



No specialised HW required, since cloud servers can emulate coded monoprocessor hardware and perform managed code execution

Coded monoprocessor – recall. Use of coded data

 $x \mapsto (x_f, x_c)$ 

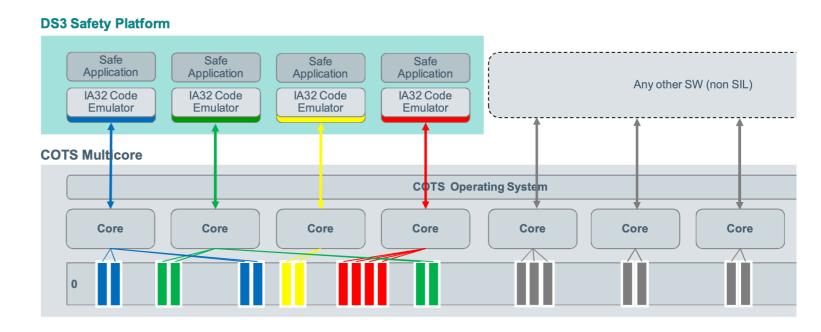
 $x_c = A \cdot x_f + B_x + D_t$ 

- A : transformation factor
- B : static signature
- D : dynamic signature

Verification of redundant channel information

$$z_c = A \cdot z_f + B_z + D_t?$$

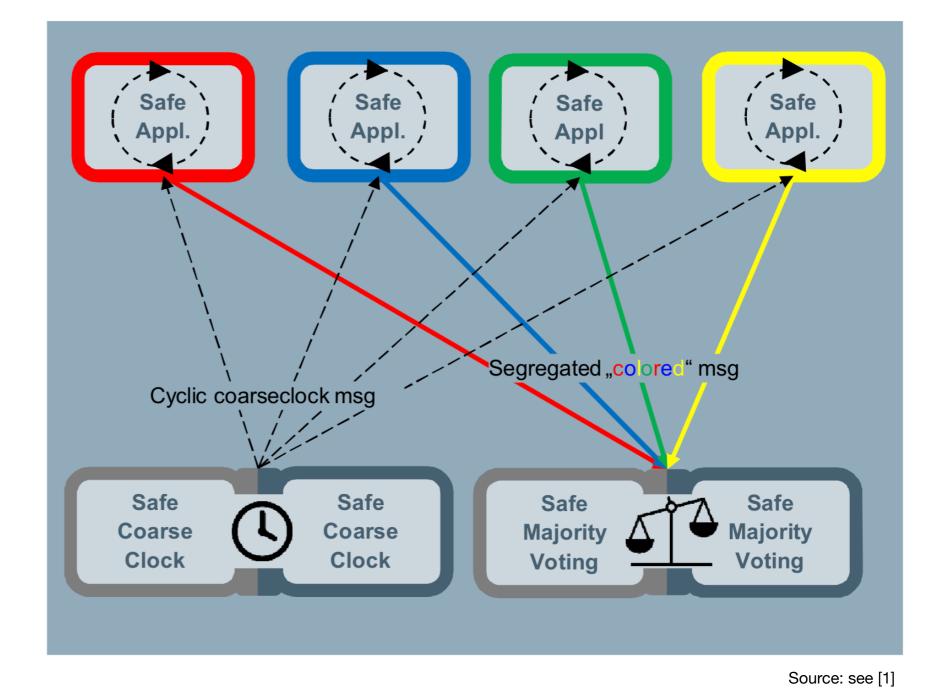
 $(z_c - B_z - D_t) \mod A = 0?$ 



**Coded monoprocessor** 

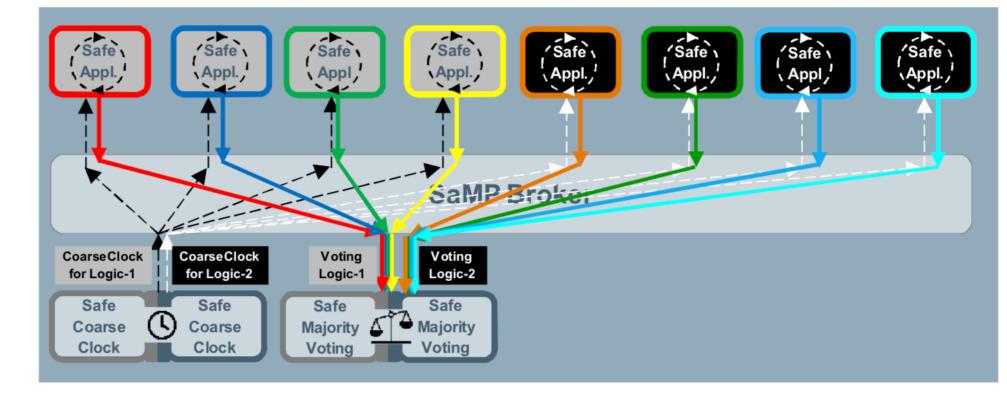
- Strict cyclic processing
- Synchronisation of redundant software components by logical clock
- Memory scattering
- Coded data and associated diverse transformation operations
- Calculation of work flow digest values
- Dynamic data signatures ensure use of the data at correct point in time
- Encryption with complementary keys ensures that data can only be used if all redundant components have calculated the equivalent result.

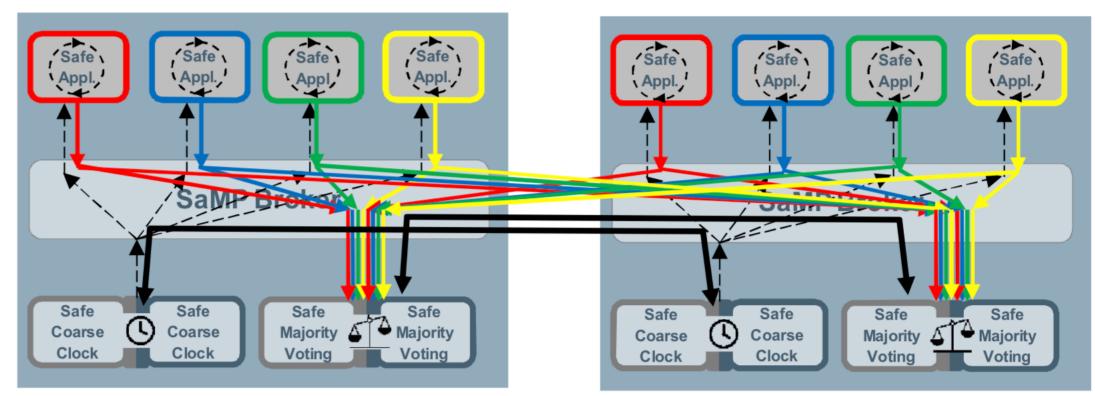
Increase **reliability** by means of n-modular redundancy and m-out-of-n voters



### Dynamic reconfiguration ...

... even across geographically distributed server farms





### Cloud-based Railway Control – V&V Challenges

# V&V Challenges: many different SW and system paradigms to be integrated

Coded Monoprocessor

Hard Real-Time Guarantees

Legacy Software Execution

Distributed Deployment

SAFE PROTOCOLS

Publish-Subscribe Pattern

n-Modular Redundancy

**Distributed Clock Synchronisation** 

MESSAGE BROKER

HW Emulation

Fail-safe Behaviour

Security Mechanisms

Generics

Dynamic Reconfiguration

Safety Software Patterns

Virtual Machines

MULTICORE PROCESSING

# V&V Challenges

- As a consequence,
  - it is very hard to create a single reference model capturing all required system aspects
  - many different V&V strategies are needed to check the correctness of requirements, design, and code

# **V&V Solutions**

# Scenario Models

- Coping with model complexity an approach adopted from the field of autonomous vehicles, see [2]
  - Identify scenarios
  - Develop collection of per-scenario models
    - Parameterised models specifying the required behaviour for a specific operational situation

## Side Remark – why Models are so Important

- Formal specifications are highly recommended according to standard EN 50128
- We need them for
  - specification validation by model checking and simulation
  - automated code generation
  - automated model-based testing
  - enabling traceability between requirements, code, tests, and other V&V artefacts

## Automated Model-based Testing

- Coping with large amount of test cases
  - Test case/test data generation and test procedure generation from models can be fully automated
  - Test suite execution may be parallelised by using cloud services

# **Complete Test Suites**

- Model-based test suites with high test strength
  - A black-box test suite is complete with respect to a given fault model if and only if
    - Every conforming SUT passes all test cases
    - Every non-conforming SUT inside the fault domain fails at least one test case

# **Complete Test Suites**

- How can we cope with the size of complete test suites?
  - Take generic parameters into account by using symbolic methods [3], [4]
  - Reduce test suites size by building equivalence classes
    [5]
  - Reduce test suite size further by enforcing completeness only for safety-related or mission-critical requirements [6]

# Conclusion

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- Cloud-based architecture for railway control systems has been presented
  - Based on the DS<sup>3</sup> system by Siemens Mobility GmbH
- V&V issues have been analysed
- Feasible modelling approach can be based on scenarios
- Test strategies with full fault coverage may be used to prove correct implementation of safety-relevant requirements with acceptable effort

### Main Challenges for the Future

- Invent validation methods to check completeness and consistency of scenario collections
- Elaborate complete strategies for testing arbitrary requirements specified in temporal logic with acceptable effort

# Further Reading

- Sonja Steffens. Safety@COTS Multicore, Distributed Smart Safe System DS<sup>3</sup>. Siemens Mobility GmbH 2018, available under <a href="https://www.google.com/url?sa=t&rct=j&q=&esrc=s&source=web&cd=1&ved=2ahUKEwizhdvT-djiAhVQr6QKHU5QDQUQFjAAegQIBRAC&url=https%3A%2F%2Fsmartrail40.ch%2Fservice%2Fdownload.asp%3Fmem%3D0%26path%3D%255Cdownload%255Cdownloads%255C2018%252011%252013%2520Innovationstag%2520ETCS%2520Stellwerk\_smartrail%25204.0.pdf&usg=AOvVaw23cALWR65rwvLr7jpjvt11</a>
- 2. Hardi Hungar: Scenario-Based Validation of Automated Driving Systems. ISoLA (3) 2018: 449-460
- 3. Jan Peleska: Model-based avionic systems testing for the airbus family. ETS 2018: 1-10
- Jan Peleska, Jörg Brauer, and Wen-ling Huang: Model-Based Testing for Avionic Systems Proven Benefits and Further Challenges. ISoLA (4) 2018: 82-103
- 5. Wen-ling Huang and Jan Peleska: Complete model-based equivalence class testing for nondeterministic systems. Formal Aspects of Computing 29(2), 335-364, 2017. doi=10.1007/s00165-016-0402-2
- 6. Wen-ling Huang, Sadik Özoguz, and Jan Peleska: Safety-complete test suites. Software Quality Journal, published online, DOI 10.1007/s11219-018-9421-y, 2018