



## Call for Papers

# DUHDe – 2nd Workshop on Design Automation for Understanding Hardware Designs

March 13, 2015 – Friday Workshop at DATE 2015, Grenoble, France

<http://www.informatik.uni-bremen.de/duhde/2015>

### SCOPE OF THE WORKSHOP AND TARGET AUDIENCE

Understanding a hardware design is tough. When entering a large team as a new member, when extending a legacy design, or when documenting a new design, a lack in understanding the details of a design is a major obstacle for productivity. In software engineering topics like software maintenance, software understanding, reverse engineering are well established in the research community and partially tackled by tools. In the hardware area the re-use of IP-blocks, the growing size of designs and design teams leads to similar problems. Understanding of hardware requires deep insight into concurrently operating units, optimizations to reduce the required area, and specially tailored functional units for a particular use.

The workshop is of interest to practitioners working in circuit design and to researchers interested in design automation.

The aim of the 2<sup>nd</sup> *Workshop on Design Automation for Understanding Hardware Designs* (DUHDe) is to consolidate the community for these topics in electronic design automation. The workshop is not limited to the following topics in design understanding but includes:

- Design descriptions from the ESL down to RTL
- Extraction of high-level properties
- Localization of code implementing specialized functionality
- Hardware design evolution: feature integration, feature interactions
- Innovative GUIs for design
- Managing documentation of hardware designs
- Analysis of interaction between hardware and software
- Formal methods for design understanding
- Scalable approaches to design understanding

### SUBMISSION INSTRUCTIONS

Prospective authors are requested to submit extended abstracts of 2 pages or full papers of 6 pages in IEEE conference style. Authors of accepted contributions are required to present their work at the workshop. Informal proceedings will be distributed electronically to the participants of the workshop; accepted papers will NOT be included in the DATE proceedings. The authors retain the copyright of their work and are free to submit extended versions to a conference or journal. Alternatively posters may be submitted that will not be contained in the informal proceedings.

Contributions have to be submitted through EasyChair:

<https://www.easychair.org/account/signin.cgi?conf=duhde2014>.

#### Important dates

Deadline for extended abstracts or full papers: ~~November 7, 2014~~ November 14, 2014

Notification of acceptance: December 1, 2014

Deadline for poster submissions: January 10, 2015

Notification for posters: January 25, 2015

Camera ready papers and posters: February 1, 2015

### COMMITTEE

**Organizers** Emmanuelle Encrenaz-Tiphene, Université Pierre et Marie Curie, Paris, France

*Email : [Emmanuelle.Encrenaz@lip6.fr](mailto:Emmanuelle.Encrenaz@lip6.fr)*

Goerschwin Fey, German Aerospace Center (DLR) and University of Bremen, Germany

*Email : [fey@informatik.uni-bremen.de](mailto:fey@informatik.uni-bremen.de)*

**Technical PC** *Eli Arbel, IBM Research, Haifa, Israel*

Lyes Benalycherif, ST Microelectronics, Grenoble, France

Valeria Bertacco, University of Michigan, Ann Arbor, USA

Raik Brinkmann, OneSpinSolutions GmbH, Munich, Germany

Franco Fummi, Università degli Studi di Verona, Italy

Masahiro Fujita, University of Tokyo, Japan

Ian G. Harris, University of California Irvine, USA

Maksim Jenihhin, Tallinn University of Technology, Estonia

Tun Li, National University of Defense Technology, Changsha, China