# ISMVL 2014

May 19th - 21st, 2014, Bremen, Germany

## Call for Papers

**General Chair**

Rolf Drechlser
*University of Bremen*

**Program Chair**

Robert Wille
*University of Bremen*

**Program Committee**

Autexier, Serge
*DFKI GmbH*

Dubrova, Elena
*Royal Institute of Technology*

Dueck, Gerhard
*University of New Brunswick*

Gaudet, Vincent
*University of Waterloo*

Hanyu, Takahiro
*Tohoku University*

Kamiura, Naotake
*University of Hyogo*

Machida, Hajime
*Int. Christian University*

Manya, Felip
*IIIA-CSIC*

Miller, Michael
*University of Victoria*

Minato, Shin-Ichi
*Hokkaido University*

Pantovic, Jovanka
*University of Novi Sad*

Rice, Jacqueline
*University of Lethbridge*

Sasao, Tsutomu
*Meiji University*

Simovici, Dan
*University of Massachusetts Boston*

Stanković, Radomir
*University of Nis*

Thornton, Mitch
*Southern Methodist University*

Waho, Takao
*Sophia University*

Yngvar, Berg
*University of Oslo*

Yuminaka, Yasushi
*Gunma University*

The Multiple-Valued Logic Technical Committee of the IEEE Computer Society will hold its 44th annual symposium in Bremen, Germany, on May 19th-21st, 2014. You are invited to submit original papers, surveys, or tutorial papers on any subject in the area of multiple-valued logic, including but not limited to:

- Algebra and Formal Aspects
- Automatic Test Pattern Generation
- Automatic Reasoning
- Boolean Satisfiability
- Circuit/Device Implementation
- Communication Systems
- Computer Arithmetic
- Data Mining
- Fuzzy Systems and Soft Computing
- Image Processing
- Logic Design and Switching Theory

- Logic Programming
- Machine Learning and Robotics
- Mathematical Fuzzy Logic
- Nanotechnology
- Philosophical Aspects
- Quantum Computing
- Quantum Cryptography
- Reversible Computation
- Signal Processing
- Spectral Techniques
- Verification

Authors should submit papers as PDF files following the IEEE style guidelines for conference proceedings. Each manuscript shall include a 50-100 word abstract and shall not exceed 6 pages. All accepted papers will be published by IEEE. The authors of selected papers will be invited after the conference to prepare an extended version of their paper to be published in the journal of Mulitple-Valued Logic and Soft Computing.

In conjunction with the conference, the International Workshop on Post-Binary ULSI Systems (ULSIWS) will be held on May 18th, 2014. A separate call will follow.

**Important Dates:**

- Abstract Deadline: November 17th, 2013 (extended)

- Paper Deadline: November 24th, 2013 (extended)

- Author Notification: February 2nd, 2014

- Camera-ready Version: March 2nd, 2014

- Conference: May 19th-21st, 2014

- ULSIWS-Workshop: May 18th, 2014

**Contact:**

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**Further information: http://www.informatik.uni-bremen.de/ismvl2014**