

# Maintenance of Formal Software Developments by Stratified Verification

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**Abstract.** The development of industrial-size software is an evolutionary process based on structured specifications. In a formal setting, specification and verification are intertwined. Specifications are amended either to add new functionality or to fix bugs detected during the verification process. In this paper we propose a system to maintain the verification of formal developments. It exploits the structure of the specification to reveal and eliminate redundant proof obligations and therefore constitutes itself a verification system in-the-large. Proofs in this system are represented as explicit proof objects allowing the system to adjust or reuse them in case the specification is changed.

## 1 Introduction

In formal software engineering it is common practice to specify the system design and the security requirements to be satisfied by the system in a structured manner. Usually the specifications of both parts are based on common auxiliary datastructures. Creating the arising proof obligations in a naive way by postulating all parts of the security requirements as theorems of the system design would result in umpteen redundant proof obligations relating to common datastructures. Exploiting the given (graph-) structure of specifications allows one to reveal this redundancy. In [2] we proposed the use of development graphs to represent defined and postulated properties of formal specifications in a logical way. We will introduce a calculus  $\mathcal{DG}$  to verify postulated properties. The calculus rules decompose conjectures between specifications into conjectures between parts of the specification and check whether some of those are already subsumed by the specification structure. We denote this activity by *verification in-the-large*. Those conjectures that can neither be further decomposed nor subsumed give rise to the proof obligations that must actually be tackled by some theorem prover, which is denoted by *verification in-the-small*.

The practice of formal software development is an *evolutionary process*. Revealed flaws give rise to changes of the specification and to the need for an update of all the proof work done before. Loosing this work would be an incalculable risk of the overall project costs for large verification tasks that arise in practice.

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Hence, we introduce a management of change based on the notion of development graphs to incrementally adjust existing proofs to a changed specification, while preserving as much information about proven conjectures as possible.

In the following section we will present the formal background of development graphs and introduce the formal calculus  $\mathcal{DG}$  to deal with proof obligations in the large. Section 3 is concerned with the computation of differences between specifications and how to update their logical representation inside the development graph. Section 4 presents the management of change for both, verification in-the-large and verification in-the-small, while we discuss its implementation in MAYA and related work in sections 5 and 5.

## 2 A Formal Notion of Development Graphs

In order to define development graphs we start with a short recapitulation of the basics of logics as they are given, for instance, in [10]. Thereby the notion of a logic is based on the notions of an *institution* and an *entailment system*.

An *institution*  $I = (\mathbf{Sign}, \mathbf{Sen}, \mathbf{Mod}, \models)$  consists of a category of signatures  $\mathbf{Sign}$ , two functors  $\mathbf{Sen}$  and  $\mathbf{Mod}$  giving respectively the set of valid sentences  $\mathbf{Sen}(\Sigma)$  and the models  $\mathbf{Mod}(\Sigma)$  for some signature, and a satisfaction relation  $\models_{\Sigma} \subseteq \mathbf{Mod}(\Sigma) \times \mathbf{Sen}(\Sigma)$  for each signature  $\Sigma$ . An *entailment system* is defined as follows:<sup>1</sup>

**Definition 1.** An entailment system  $\mathcal{E} = (\mathbf{Sign}, \mathbf{Sen}, \vdash)$  consists of a category  $\mathbf{Sign}$  of signatures, a functor  $\mathbf{Sen}: \mathbf{Sign} \rightarrow \mathbf{Set}$  giving the set of sentences over a given signature, and for each  $\Sigma \in |\mathbf{Sign}|$ , an entailment relation  $\vdash_{\Sigma} \subseteq |\mathbf{Sen}(\Sigma)| \times \mathbf{Sen}(\Sigma)$  such that the following properties are satisfied:

1. reflexivity: for any  $\varphi \in \mathbf{Sen}(\Sigma)$ ,  $\{\varphi\} \vdash_{\Sigma} \varphi$ ,
2. monotonicity: if  $\Gamma \vdash_{\Sigma} \varphi$  and  $\Gamma' \supseteq \Gamma$  then  $\Gamma' \vdash_{\Sigma} \varphi$ ,
3. transitivity: if  $\Gamma \vdash_{\Sigma} \varphi_i$ , for  $i \in I$ , and  $\Gamma \cup \{\varphi_i \mid i \in I\} \vdash_{\Sigma} \psi$ , then  $\Gamma \vdash_{\Sigma} \psi$ ,
4.  $\vdash$ -translation: if  $\Gamma \vdash_{\Sigma} \varphi$ , then for any  $\sigma: \Sigma \rightarrow \Sigma'$  in  $\mathbf{Sign}$ ,  $\sigma[\Gamma] \vdash_{\Sigma'} \sigma(\varphi)$ .

A logic is then defined as a 5-tuple  $\mathcal{LOG} = (\mathbf{Sign}, \mathbf{Sen}, \mathbf{Mod}, \vdash, \models)$  such that: (1)  $(\mathbf{Sign}, \mathbf{Sen}, \mathbf{Mod}, \models)$  is an institution (denoted by  $inst(\mathcal{LOG})$ ), (2)  $(\mathbf{Sign}, \mathbf{Sen}, \vdash)$  is an entailment system (denoted by  $ent(\mathcal{LOG})$ ), and (3) the following *soundness condition* is satisfied: for any  $\Sigma \in |\mathbf{Sign}|$ ,  $\Gamma \subseteq \mathbf{Sen}(\Sigma)$  and  $\varphi \in \mathbf{Sen}(\Sigma)$ ,  $\Gamma \vdash_{\Sigma} \varphi$  implies  $\Gamma \models_{\Sigma} \varphi$ . Throughout the rest of the paper, we will work with an arbitrary but fixed logic  $\mathcal{LOG} = (\mathbf{Sign}, \mathbf{Sen}, \mathbf{Mod}, \vdash, \models)$ .

Structured specifications are represented on a logical basis as development graphs. The nodes of such a graph represent individual theories. Definition links are used to specify theory inclusions (with respect to some morphism) between different theories. The axiomatic specification of a single theory is distributed to the subgraph of the corresponding node since the definition of the theory of a

<sup>1</sup> We present the notion of an entailment system in more detail as its precise definition is needed in the rest of the paper.

node depends on the local axioms attached to the node combined with the axioms or theories of the nodes imported by definition links. Since the semantics of each theory has to be well-defined, the graph of definition links must be acyclic.

In order to formulate proof obligations denoting properties between different theories (*verification in-the-large*) we introduce so-called theorem links. These links are similar in appearance to definition links but do not influence the theories denoted by the nodes. However, theorem links may form a cyclic graph. For instance, postulating that two theories are equivalent results in two theorem links going in opposite directions. Formally we define

**Definition 2.** A development graph  $\mathcal{S}$  is a directed graph  $\langle \mathcal{N}, \Psi \rangle$  that is inductively defined by

- $\mathcal{N}$  is a finite set of nodes. Each node  $N \in \mathcal{N}$  is a pair  $(\Sigma_i^N, \Phi_i^N)$  consisting of a **local signature**  $\Sigma_i^N$  and a set of **local axioms**  $\Phi_i^N \subseteq \text{Sen}(\Sigma^N)$  of  $N$ .
- $\Psi = \Psi_D \uplus \Psi_T$  is a finite set of directed links between elements of  $\mathcal{N}$  consisting of an acyclic<sup>2</sup> set  $\Psi_D$  of **definition links** and a set  $\Psi_T$  of **theorem links**. Each link from a node  $M$  to a node  $N$  in  $\Psi$  is either **global** (denoted  $M \xrightarrow{\sigma} N$ ) or **local** (denoted  $M \xrightarrow{\sigma} N$ ) and is annotated with a signature morphism  $\sigma : \Sigma^M \rightarrow \Sigma^N$ .
- For all  $N \in \mathcal{N}$  the **signature**  $\Sigma^N$  of  $N$  is given by:  
 $\Sigma^N = \Sigma_i^N \cup \{\sigma(f) \mid f \in \Sigma^M, M \xrightarrow{\sigma} N \in \Psi_D\} \cup \{\sigma(f) \mid f \in \Sigma_i^M, M \xrightarrow{\sigma} N \in \Psi_D\}$

For the implementation, we represent a signature morphism  $\sigma$  by a set of finite pairs  $(f_{in}, f_{out})$  with  $\sigma(f) = g$  if there is a pair  $(f, g) \in \sigma$  and  $\sigma(f) = f$  otherwise.

The proof theoretical semantics of a development graph is given by the following definition:

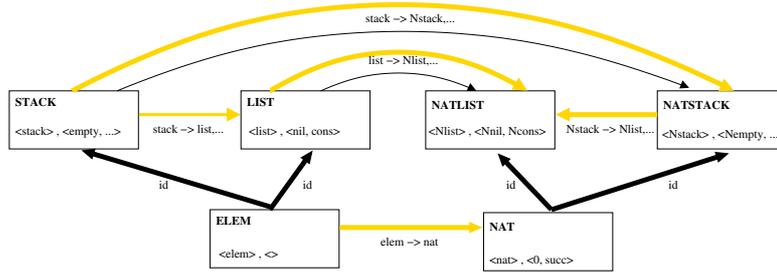
**Definition 3.** Let  $\mathcal{S} = \langle \mathcal{N}, \Psi \rangle$  be a development graph and  $\Delta \subseteq \Psi$ ,  $\Delta$  acyclic. Let  $N \in \mathcal{N}$ , then the **theory**  $Th_\Delta(N)$  of  $N$  **relative to**  $\Delta$  is defined by

$$Th_\Delta(N) = \left[ \Phi_i^N \cup \bigcup_{K \xrightarrow{\sigma} N \in \Delta} \sigma(Th_\Delta(K)) \cup \bigcup_{K \xrightarrow{\sigma} N \in \Delta} \sigma(\Phi_i^K) \right]^{\vdash_{\Sigma^N}}$$

where  $[\Gamma]^{\vdash_{\Sigma^N}}$  denotes the closure of  $\Gamma$  under the entailment relation  $\vdash_{\Sigma^N}$ . The **theory**  $Th(N)$  of  $N$  is defined as  $Th_{\Psi_D}(N)$ .

Fig. 1 presents a development graph for lists LIST and stacks STACK over arbitrary elements and their respective instantiations to lists NATLIST and stacks NATSTACK over natural numbers. While we included the local signatures of the nodes in the figure we have omitted the local axioms because of shortage of space. The theories of generic lists LIST and generic stacks STACK are defined with the help of a theory ELEM, indicated by the global definition links from ELEM to LIST and STACK, and local axioms in LIST and STACK specifying that

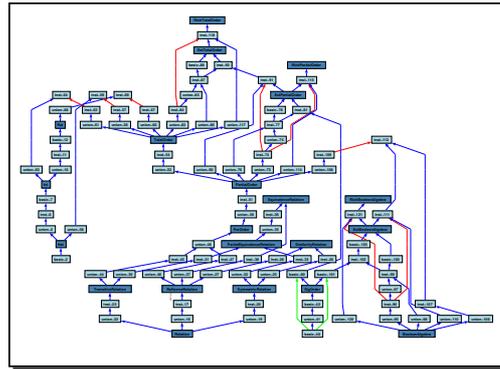
<sup>2</sup> A set of links is acyclic iff the graph denoted by these links is acyclic.



**Fig. 1.** Structured Specifications of NATLIST

LIST and STACK are freely generated. The global theorem link between STACK and LIST represents the proof obligation, that STACK can be implemented by LIST.

The theories of lists and stacks of natural numbers, NATLIST and NATSTACK, are instantiations of generic lists and stacks with natural numbers NAT. Thus, both NATLIST and NATSTACK import NAT via a global definition link and the local axioms of LIST and STACK respectively via local definition links. The global theorem links between LIST and NATLIST, and STACK and NATSTACK denote the proof obligations that NATLIST and NATSTACK are respective instances of LIST and STACK. The global theorem link between ELEM and NAT denotes the proof obligation that the actual parameter NAT satisfies the requirements of the formal parameter ELEM. The proof obligation that NATSTACK can be implemented by NATLIST is represented by a global theorem link from NATSTACK to NATLIST, denoting that all theorems about NATSTACK, mapped to the signature of NATLIST, are theorems of NATLIST.



**Fig. 2.** Example of development graphs for real software engineering problems

This toy example illustrates how the important concepts from structured specifications are represented with development graphs. In practice the system and requirement specifications and hence the resulting development graph are much larger. A development graph of a typical size is sketched in Fig. 2.

The theory of a node  $N$  depends on theories of all nodes connected to  $N$  via definition links. Local definition links import only the local axioms of the source node and therefore hide the theories of underlying subnodes. In the example we use this to define the actualizations of the parameterized specifications of NATLIST and NATSTACK. Using a local definition link from LIST to NATLIST imports only the local axioms of LIST but not the specification of the parameter type ELEM. Thus postulating the global theorem link from LIST to NATLIST

corresponds to the proof obligation that the mapped axioms of ELEM are theorems in NATLIST, i.e. NAT (as part of NATLIST) satisfies the requirements of the parameter specification.

The next definition specifies possible paths to include the theory or the local axioms of the source node to the theory of the target node.

**Definition 4.** *Let  $\Psi$  be a set of links.*

- $\Psi$  contains a **global path**  $N_1 \xrightarrow{\sigma}_{\Psi} N_k$  from  $N_1$  to  $N_k$  via a morphism  $\sigma$  if there is either a sequence of links  $N_1 \xrightarrow{\sigma_1} N_2, N_2 \xrightarrow{\sigma_2} N_3 \dots N_{k-1}, \xrightarrow{\sigma_{k-1}} N_k$  in  $\Psi$  with  $\sigma = \sigma_1 \circ \dots \circ \sigma_{k-1}$  or  $N_1 = N_k$  and  $\sigma$  is the identity function.
- $\Psi$  contains a **local path**  $N_1 \xrightarrow{\sigma}_{\Psi} N_k$  from  $N_1$  to  $N_k$  via a morphism  $\sigma$  if there is a sequence of links  $N_1 \xrightarrow{\sigma_1} N_2, N_2 \xrightarrow{\sigma_2} N_3 \dots N_{k-1}, \xrightarrow{\sigma_{k-1}} N_k$  in  $\Psi$  with  $\sigma = \sigma_1 \circ \dots \circ \sigma_{k-1}$ .

Given a development graph  $\langle \mathcal{N}, \Psi_D \uplus \Psi_T \rangle$ , the definition links  $\Psi_D$  are used to specify the semantics, i.e. theory, of the individual nodes.  $\Psi_T$  constitutes the proof obligations inside the graph. In the following we define when a development graph satisfies these proof obligations:

**Definition 5.** *Let  $\mathcal{S} = \langle \mathcal{N}, \Psi \rangle$  be a development graph and  $\Delta \subseteq \Psi$  be acyclic.  $\Delta$  satisfies a link  $M \xrightarrow{\sigma} N \in \Psi$  (or  $M \xrightarrow{\sigma} N \in \Psi$  resp.) iff  $\sigma(Th_{\Delta}(M)) \subseteq Th_{\Delta}(N)$  (or  $\sigma(\Phi_l^M) \subseteq Th_{\Delta}(N)$  resp.).  $\Delta$  satisfies a set  $\Gamma$  of links if it satisfies all elements in  $\Gamma$ .*

*A development graph  $\mathcal{S} = \langle \mathcal{N}, \Psi_D \uplus \Psi_T \rangle$  is **verified** iff  $\Psi_D$  satisfies  $\Psi_T$ .*

A global definition link includes the theory of the source node into the theory of the target node while a local definition link includes only the local axioms of the source node. Due to the  $\vdash$ -translation property (cf. Def. 1), any global definition link starting at the target node of such a link will export this imported theory or axioms in turn to other theories. Theorem links which are satisfied by the definition links can be treated in the same manner as definition links:

**Lemma 1.** *Let  $\mathcal{S} = \langle \mathcal{N}, \Psi_D \uplus \Psi_T \rangle$  be a development graph and let  $\Psi_D$  satisfy a set of links  $\Delta$ . Then the following holds:*

1.  $N \xrightarrow{\sigma}_{\Psi_D \uplus \Delta} M$  implies  $\sigma(Th(N)) \subset Th(M)$  and
2.  $N \xrightarrow{\sigma}_{\Psi_D \uplus \Delta} M$  implies  $\sigma(\Phi_l^N) \subset Th(M)$

*Proof.* The proof is by induction over length of the paths (cf. appendix).  $\square$

In order to verify a development graph we introduce a calculus  $\mathcal{DG}$  operating on links to perform a so-called *verification in-the-large* and providing a *local decomposition rule* to establish elementary relations between theories by usual theorem proving, which we call *verification in-the-small*.

**Definition 6 (Calculus  $\mathcal{DG}$ ).** *The calculus  $\mathcal{DG}$  is a sequent-style calculus. Sequents are of the form  $\Gamma \vdash \Delta$ , where  $\Gamma, \Delta$  are sets of links. A sequent  $\Gamma \vdash \Delta$  holds iff  $\Gamma$  satisfies  $\Delta$ . The sequent calculus rules of  $\mathcal{DG}$  are:*

Axiom (AX):  $\overline{\Gamma \vdash \emptyset}$

Global decomposition (GD):

$$\frac{\Gamma \vdash N \xrightarrow{\sigma} M, \bigcup_{K \xrightarrow{\rho} N \in \Gamma} \{K \xrightarrow{\sigma \circ \rho} M\}, \bigcup_{K \xrightarrow{\rho} N \in \Gamma} \{K \xrightarrow{\sigma \circ \rho} M\}, \Delta}{\Gamma \vdash N \xrightarrow{\sigma} M, \Delta}$$

Local decomposition (LD):

$$\frac{\Gamma \vdash \Delta}{\Gamma \vdash N \xrightarrow{\sigma} M, \Delta} \quad \text{if for all } \phi \in \Phi_i^N : \sigma(\phi) \in Th_\Gamma(M)$$

Global subsumption (GS):

$$\frac{\Gamma \vdash \Delta}{\Gamma \vdash N \xrightarrow{\sigma} M, \Delta} \quad \text{if } N \xrightarrow{\sigma} \Gamma \cup \Delta M$$

Local subsumption (LS):

$$\frac{\Gamma \vdash \Delta}{\Gamma \vdash N \xrightarrow{\sigma} M, \Delta} \quad \text{if } N \xrightarrow{\sigma} \Gamma \cup \Delta M$$

**Theorem 1.** Let  $\mathcal{S} = \langle \mathcal{N}, \Psi_D \uplus \Psi_T \rangle$  be a development graph and  $\Delta \subseteq \Psi_T$ . Then,  $\Psi_D \vdash \Delta$  is derivable in the deduction system  $\mathcal{DG}$  iff  $\Psi_D$  satisfies  $\Delta$ .

*Proof.* The soundness proof is done by induction over the length of the derivation. The completeness proof is done by induction over the multiset of the depths of the source nodes of theorem links (cf. appendix for the complete proofs).  $\square$

The  $\mathcal{DG}$ -calculus is based on an (external) prover to check if  $\sigma(\phi) \in Th_{\Psi_D}(M)$  holds. In general  $Th_{\Psi_D}(M)$  is an infinite set of formulas and we need a finite axiomatization for it. It is well-known that structured specifications excluding hiding<sup>3</sup> are flatable. The following lemma describes the finite axiomatization of the theory of a node:

**Lemma 2.** Let  $\mathcal{S} = \langle \mathcal{N}, \Psi \rangle$  be a development graph and let the **axiomatization** of some node  $N \in \mathcal{N}$  **relative** to some  $\Delta \subseteq \Psi$ ,  $\Delta$  acyclic, be defined by

$$\Phi_\Delta^N = \Phi_i^N \cup \bigcup_{K \xrightarrow{\sigma} N \in \Delta} \sigma(\Phi_\Delta^K) \cup \bigcup_{K \xrightarrow{\sigma} N \in \Delta} \sigma(\Phi_i^K)$$

Then,  $Th_\Delta(N) = [\Phi_\Delta^N]^\vdash_{\Sigma^N}$  holds for all  $N \in \mathcal{N}$ .

*Proof.* Directly from Def. 3 and the  $\vdash$ -translation property (cf. Def. 1).  $\square$

To verify the proof obligations on local theorem links, which we call *verification in-the-small*, we make use of standard theorem provers like ISABELLE [12] or INKA 5.0 [1]. The reader is referred to [3] for a description how development graph and theorem provers are technically connected.

<sup>3</sup> See [11] for an extension of development graphs by hiding which translates proof obligations in theories based on hiding to proof obligations in theories without hiding.

### 3 Difference Analysis & Basic Operations

Due to its evolutionary nature, (formal) software development can be seen as a chain of specifications  $Spec_1, Spec_2, \dots$  which corresponds to a chain of development graphs  $DG_1, DG_2, \dots$  such that  $DG_i$  is the logical representation of the specification  $Spec_i$ . Working on the verification side we try to verify the various proof obligations within a particular development graph, say  $DG_i$ . Changing the specification to  $Spec_{i+1}$  and compiling it into its logical representation  $DG_{i+1}$ , we loose all information about previous proof work, which is stored in  $DG_i$ , at first. Hence, the idea is to incrementally adjust  $DG_i$  and its annotated proofs until the resulting development graph  $DG_{i+1}$  denotes a logical representation of  $Spec_{i+1}$ . Two problems have to be solved to implement this approach:

First, we need a set of operations which allow us to modify development graphs in such a way that as much proof work as possible can be reused from the previous development graph. We call these operations, that manipulate individual links, theories or axioms, *basic operations*.

Second, we have to compute the differences between two specifications  $Spec_i$  and  $Spec_{i+1}$  and translate these differences into a sequence of basic operations to be performed on the development graph  $DG_i$  in order to obtain  $DG_{i+1}$ .

#### 3.1 Basic Operations

To allow for a reuse of proof work, basic operations have to be as granular as possible. Since development graphs consists of nodes and links, basic operations allow one to modify single nodes or links. In principle each of these individual objects can be inserted, deleted or modified. As nodes are composed of a local signature and local axioms, the modification of nodes is done by insertion, deletion or modification of signature entries or local axioms. Formally the set of basic operations consists of the following functions that take, between others, a development graph  $\mathcal{S} = \langle \mathcal{N}, \Psi_D \uplus \Psi_T \rangle$  as argument and return a new development graph  $\mathcal{S}'$ :

**Nodes:**  $ins_{node}(N, \mathcal{S})$  inserts a new (isolated) node  $N$  to  $\mathcal{N}$ , and  $del_{node}(N, \mathcal{S})$  removes a node  $N$  from  $\mathcal{N}$  and deletes also all links in  $\Psi_D$  and  $\Psi_T$  connected to  $N$ .

**Links:**  $ins(N, M, \sigma, Type, \mathcal{S})$  inserts a link to  $\Psi$  as a global/local definition/theorem link depending on the value of  $Type$ .  $del(L, \mathcal{S})$  removes the link  $L$  from  $\Psi_D \uplus \Psi_T$ , and  $ch(L, \sigma, \mathcal{S})$  replaces the morphism of the link  $L$  by  $\sigma$ .<sup>4</sup>

**Local Signature:**  $ins_{sig}(f, N, \mathcal{S})$  inserts the symbol  $f$  into the local signature of  $N$ , where  $f$  can be either a sort, a constant, or a function.  $del_{sig}(f, N, \mathcal{S})$  removes the symbol  $f$  from the local signature of  $N$ .

**Local Axioms:**  $ins_{ax}(N, Ax, \mathcal{S})$  inserts the local axiom  $Ax$  into the node  $N$ ,  $del_{ax}(f, N, \mathcal{S})$  deletes the local axiom  $Ax$  from the node  $N$ .  $ch_{ax}(N, Ax, Ax', \mathcal{S})$  replaces the local axiom  $Ax$  by the new local axiom  $Ax'$  in the node  $N$ .

<sup>4</sup> There are no operations to change the source or target node of a link. In this case the old link must be deleted and a new link is inserted.

For each basic operation the manner how it affects the development graph is known. This knowledge is exploited by the proof transformation techniques, that adapt the proofs of old global proof obligations to the new global proof obligations. We will describe these techniques in the following section.

Starting with a legal development graph, the application of basic operations may result in inconsistent intermediate states. A typical example is the insertion of a new function symbol into the source node of a link. Then in general, the morphism attached to the link has to be adjusted to cope with the new symbol. Therefore we allow for intermediate inconsistent states of the development graph and delay the update of the proof work until we reach a consistent state which is indicated by calling a special *update*-function initiating a consistency check and a propagation of the proof work done so far.

### 3.2 Computing Differences

When computing differences between specifications, the question arises how to define the granularity up to which differences are determined between the old and the new development graph. Note that along a scale of granularity levels for difference analysis the worst granularity level is the one only stating that the whole global proof obligation changed, in which case the proof transformation consists of redoing the whole proof, whereby any information about established conjectures are lost.

The overall aim is to enable the preservation of as many validated conjectures during the transformation of the old proof to the new development graph. The recorded information establishing the validity of a conjecture consists of proofs for those conjectures. However, not every theorem prover returns a proof object. In that case, we must assume that any axiom available at prove time might have been used during the proof. Thus, the information about a proof contains at least a set of axioms. If any of those is deleted or changed, the proof gets invalid. The implication is that we have to determine the difference between the old and new development graph at least on the level of axioms.

The axioms are build from the available signature symbols, like sorts, constants and functions. In order to maintain a sound development graph, we must also be able to determine the differences between signatures. As presented in Sect. 2, the signature of some node is defined from the local signature defined on that node and the signatures of the nodes imported via definition links, after application of the morphism attached to those links.

To determine the differences of signatures and axioms between two development graphs requires first to define an equivalence relation between graphs that identifies nodes and links. This problem has no optimal solution and hence we rely on some heuristics checking their equivalence. In principle two nodes are equivalent if their local signature and axioms are equal as well as their respective incoming definition links. However, this equivalence relation is too strict for our purpose, since if we added or deleted an axiom to some node, its old and new version are not identified. Thus, instead of performing an equality check, we perform a similarity check on nodes, that is based on the number of shared local

signature symbols as well as the similarity of the incoming definition links. Applying that similarity check results in an equivalence relation associating nodes and links of the old to nodes and links in the new development graph.

The equivalence relation is the basis to determine the differences between both graphs. From it we determine (1) which nodes have been deleted or added, (2) which local signature symbols and axioms have been deleted or added to some node, and (3) how the morphisms of links have changed.

## 4 Maintaining Proof Work

The development graph represents a justification-based truth maintenance system for structured specifications. Based on underlying theorem provers it provides justifications for proof obligations (encoded as theorem links) and is able to remember and adjust derivations which were computed previously. There are two different types of justifications corresponding to the verification in-the-large and to the verification in-the-small which both have to be updated each time the graph is changed. In the following we describe this propagation of proof work for the verification in-the-large and the verification in-the-small separately. Both parts are implemented into the MAYA-system and are done completely automatic once the specification is changed.

### 4.1 Verification In-the-Large

Verification in-the-large is concerned with the overall problem of proving that  $\Psi_D$  satisfies all theorem links in  $\Psi_T$ . To support the maintenance of the proofs, we annotate theorem links with explicit proof objects, which are instances of the  $\mathcal{DG}$ -calculus rules. Each  $\mathcal{DG}$ -calculus rule reduces the proof of a theorem link to the problem of proving a set of other theorem links. Thus, the proof object of a theorem link is distributed through the development graph and only the first inference step, the so-called *local proof object*, is stored at the theorem link while the remaining part coincides with proof objects of other theorem links.

**Definition 7.** Let  $\psi = N \xrightarrow{\sigma} M$  then

- $pr_\psi := GD(\psi_0, \langle \psi'_1, \dots, \psi'_n \rangle, \langle \psi''_1, \dots, \psi''_m \rangle)$  is a local proof object.  $pr_\psi$  is locally valid iff  $\psi_0 = N \xrightarrow{\sigma} M$ ,  $\{\psi'_1, \dots, \psi'_n\} = \bigcup_{K \xrightarrow{\rho} N \in \Gamma} \{K \xrightarrow{\sigma \circ \rho} M\}$  and  $\{\psi''_1, \dots, \psi''_m\} = \bigcup_{K \xrightarrow{\rho} N \in \Gamma} \{\psi \xrightarrow{\sigma \circ \rho} M\}$
- $pr_\psi := GS(\psi_1, \dots, \psi_n)$  is a local proof object.  $pr_\psi$  is locally valid iff  $\psi_1, \dots, \psi_n$  constitutes a relation  $N \xrightarrow{\sigma} M$ .

Let  $\psi = N \xrightarrow{\sigma} M$  then

- $pr_\psi := LS(\psi_1, \dots, \psi_n)$  is a local proof object.  $pr_\psi$  is locally valid iff  $\psi_1, \dots, \psi_n$  constitutes a relation  $N \xrightarrow{\sigma} M$ .

- $pr_\psi := LD(\sigma, (Ax_1, \Phi_1), \dots, (Ax_k, \Phi_k))$  is a proof object where each  $\Phi_i$  is either an atom *NoProof*, *ProofExists* or a set of triples  $(\tau, K, \Omega)$  with  $\Omega \subset \Phi_i^K$ .  $pr_\psi$  is locally valid iff for all  $(Ax_i, \Phi_i)$  with  $1 \leq i \leq n$ ,  $(\bigcup_{(\tau, K, \Omega) \in \Phi_i} \tau(\Omega)) \vdash \sigma(Ax_i)$  and for all triple  $(\tau, K, \Omega) \in \Phi_i$   $K \xrightarrow{\tau} M$  holds.

$\Psi(pr_\psi)$  is defined as the set of all links occurring in the proof object  $pr_\psi$  of  $\psi$ .  $\Psi^*(pr_\psi)$  denotes the transitive closure of  $\Psi(pr_\psi)$  and is defined by  $\Psi^*(pr_\psi) = \Psi(pr_\psi) \cup \bigcup_{\psi' \in \Psi(pr_\psi)} \Psi^*(pr_{\psi'})$

**Lemma 3.** *Let  $\mathcal{S} = \langle \mathcal{N}, \Psi_D \uplus \Psi_T \rangle$  be a development graph. If there are valid proof objects  $pr_\psi$  with  $\psi \notin \Psi^*(pr_\psi)$  for all  $\psi \in \Psi_T$  then  $\Psi_D$  satisfies  $\Psi_T$ .*

Verification in-the-large is concerned with the problem of creating and maintaining local proof objects of the types GD, GS and LS such that each of these local proof objects are locally valid and such that the proof object of a link  $\psi$  does not depend on itself, i.e.  $\psi \notin \Psi^*(pr_\psi)$ . The problem of maintaining LD-proof objects is discussed in section 4.2. We call a development graph verified in-the-large if and only if all GD, GS, LS-proof objects are locally valid and do not contain cycles (i.e.  $\psi \notin \Psi^*(pr_\psi)$ ).

Starting with an empty development, which is trivially verified, the graph is manipulated by using basic operations like for instance the insertion, deletion, or change of links or axioms. After a sequence of basic operations (performing the change of the specification made by the user) the proof objects are adapted to the needs of the actual graph. Hence, each subsequent development graph is verified reusing the old proof objects annotated in the former development graph.

To describe the update-process, assume now that we manipulated a verified development graph with the help of a sequence of basic operations. To establish the validity of the resulting development graph we perform the following steps:

*Checking GD-proof objects:* In the first phase, existing GD-proof objects are updated to be locally valid proof objects. Starting at the top-level theories (like LIST in our example), we traverse the graph according to the depth of the theories. Reasons for an invalidated GD-proof object  $pr_\psi$  are the change of the morphism of some link or the insertion or deletion of definition links targeting at the source of the theorem link. In the first case we replace an inappropriate link by a link with an appropriate morphism. Either such a link already exists (e.g. as a definition link) or it is created and added to  $\Psi_T$  while it inherits the (invalid) proof object of the replaced link (this proof object will be fixed in the ongoing procedure). In case of insertion or removal of definition links, both link lists in a proof object  $pr_\psi$  are updated accordingly. This, again, might result in the creation of new theorem links, which are again added to  $\Psi_T$ , or the deletion of theorem links from  $\Psi_T$  if they have been once created using the GD-rule and are of no use anymore (i.e. they do not occur in any proof object anymore).

*Checking GS- and LS-proof objects:* In the second phase, proof objects concerned with subsumption rules are checked for validity. For each of these proof objects

$pr_\psi$  we prove whether all links in  $\Psi(pr_\psi)$  do still exist and whether the morphism of the denoted path still coincides with the morphism of the theorem link  $\psi$ . If any of these conditions fails then the proof object is removed; otherwise the proof object  $pr_\psi$  is still locally valid.

*Establish new proof object* In the third phase local proof objects are generated for theorem links which do not possess any proof object. Either these links have been newly created or their proof objects have been removed in an earlier stage of the procedure. Given a theorem link  $\psi$ , firstly we search for an application of the GS- or LS-rule. Thus, we search for a path starting at the source of  $\psi$  and ending at the target of  $\psi$  which coincides with  $\psi$  also in its morphism. In order to obtain an acyclic proof object, each link  $\psi'$  in the path has to satisfy the property  $\psi \notin \Psi^*(pr_{\psi'})$ . In practice we restrict this search for a path inside a graph in the following way: First, we do not search for paths in which a node is visited twice (although in general, running through a circle may result in a different overall morphism of the path). Second, proving a theorem link  $K \xrightarrow{\sigma \circ \rho} M$  which was created while verifying a theorem link  $N \xrightarrow{\sigma} M$  in presence of a definition link  $K \xrightarrow{\rho} N$ , we do not consider paths starting with this definition link. If we would find such a path then we could strip off the definition link to obtain a path for  $N \xrightarrow{\sigma} M$  (but this was already checked during the verification of this link!). If we cannot find a suitable path to establish a GS- or LS-proof object, a GD-proof object for  $\psi$  is generated. This may cause the generation of new theorem links to be added to  $\Psi_T$  if no suitable links are already available in the graph.

To illustrate our approach, consider our example in Fig. 3. As we have started with the empty development graph there are no GD, GS or LS-proof objects to be updated and we continue with phase three:

Descending the graph according to the depth of the theories, we first establish a new proof object for the global theorem link from LIST to NATLIST. The GS-rule is not applicable since there is no corresponding global path from LIST to NATLIST. Hence, the GD-rule is applied which results in a proof object  $GD(\psi_0, \langle \psi_{elem} \rangle, \langle \rangle)$ .  $\psi_0$  is the local definition link from LIST to NATLIST while  $\psi_{elem}$  is a newly generated theorem link from ELEM to NATLIST (corresponding to the import of ELEM in LIST). Similarly, we obtain a local proof object  $GD(\psi'_0, \langle \psi'_{nat} \rangle, \langle \psi'_{stack} \rangle)$  for the global theorem link  $\psi'$  from NATSTACK to NATLIST.  $\psi'_0$  is a newly generated local theorem link parallel to  $\psi'$ ,  $\psi'_{nat}$  is the global definition link from NAT to NATLIST.  $\psi'_{stack}$  is a newly generated local theorem link from STACK to NATLIST. Using the LS-rule  $\psi'_{stack}$  is proven by the path of (global) theorem links from STACK over LIST to NATLIST. Since NATSTACK has no local axioms,  $\psi'_{nat}$  is trivially proven using the LD-rule. Applying the GD-rule to the global theorem link from STACK to NATSTACK introduces a global theorem link from ELEM to NATSTACK which is proven using the GS-rule by the path of global links from ELEM over NAT to NATSTACK. At the end we are left with open proofs for the local theorem links from STACK to LIST and from ELEM to NAT which are tackled by the verification in-the-small.

Suppose now, we change the graph structure by the insertion of a new theory REL introducing a new symbol  $R$  and imported by ELEM. Therefore all morphisms of the links from ELEM, LIST and STACK to NAT, NATLIST and NATSTACK will be changed in order to incorporate an appropriate mapping of  $R$ . In the first phase of the revision process the GD-proof objects of the corresponding global theorem links are adjusted to incorporate the mapping of  $R$ . Additionally, the proof object of the global theorem link from ELEM to NAT is changed to  $GD(\psi'_0, \langle \psi_{REL} \rangle, \langle \rangle)$  where  $\psi_{REL}$  denotes a global theorem link from REL to NAT (corresponding to the new definition link from REL to ELEM). In the second phase nothing has to be done since all GS- and LS-proof objects are still valid although the mapping have changed. In the third phase the new theorem link  $\psi_{REL}$  is proven with the help of the GD-rule which introduces a local theorem link from REL to NAT denoting the proof obligations arising from the local axioms in REL to be proven in NAT.

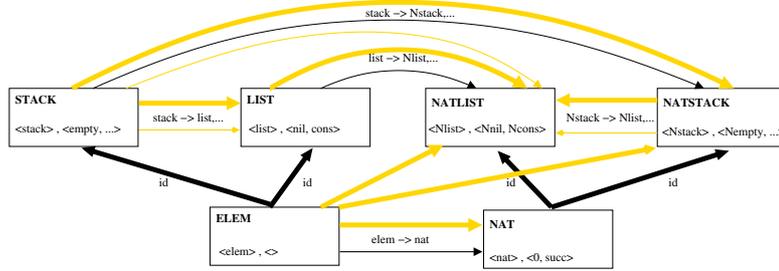


Fig. 3. Management of change for NATLIST

## 4.2 Verification In-the-Small

Applying the local decomposition (LD-)rule gives rise to proof obligations that each local axiom of the source node mapped by the attached morphism of the link is a theorem of the target theory. To tackle these proof obligations, the system has to compute the axiomatization of the theory (ref. lemma 2) and to apply the morphism of the theorem link to the axioms of the source theory. Since the computation of the axiomatization is expensive the system caches the computed axiomatization of the target node. The axiomatization is annotated by the information about the origin, applied morphisms and used paths of mapped axioms. Once the axiomatization of a different node is needed to tackle another proof obligation, the path information attached to the cached axiomatization is used to incrementally compute the axiomatization of the new node by comparing the needs with the annotated information. Thus, we obtain a set of axioms to be removed from the cached axiomatization and a set of axioms to be inserted to the cached axiomatization to obtain the axiomatization of the new node.

Suppose  $\psi = N \xrightarrow{\sigma} M$  is a local theorem link with an attached local proof object  $LD(\sigma, (Ax_1, \Phi_1), \dots, (Ax_k, \Phi_k))$ . Each axiom  $Ax_i$  of  $N$  is related to the

proof description  $\Phi_i$ .  $\Phi_i$  is either an atom NoProof indicating that this proof obligation has not been proven yet, or an atom ProofExists indicating that some theorem prover has proven the problem but did not return an explicit proof object, or the set of axioms used to prove  $\sigma(Ax_i)$  inside the theory of  $M$ . In this case  $\Phi_i$  breaks down the used axioms according to their origins and the morphism with the help of which they are imported to the target theory.

Changing either the axioms of  $N$ , the morphism  $\sigma$  or the subgraph of  $M$  may render the proof object  $pr_\psi$  invalid. In the following we discuss the repair of the proof object  $pr_\psi$  for these three cases:

*Change in  $N$ :* The change of source axioms results in corresponding changes of the proof obligations. Insertion of a new axiom  $Ax_{k+1}$  will result in a new entry  $(Ax_{k+1}, \text{NoProof})$ , where NoProof indicates that  $\sigma(Ax_{k+1})$  is still to be proven by some theorem prover. Deletion of some  $Ax_i$  will result in the removal of the corresponding pair  $(Ax_i, \Phi_i)$ . Change of a source axiom  $Ax_i$  to  $Ax'_i$  causes an invalidation of  $\Phi_i$ . If the system provides explicit proof objects (instead of the set of used axioms) the system supports the theorem prover by additionally providing  $Ax_i$  and the old proof for  $\sigma(Ax_i)$  when proving  $\sigma(Ax'_i)$  to allow for a reuse of the old proof.

*Change in morphism  $\sigma$ :* Changing the morphism  $\sigma$  attached to the theorem link to  $\sigma'$  may result in a change of some proof obligations depending how the change of the morphism affects the mapping of local axioms of the source theory. If  $\sigma(Ax_i) = \sigma'(Ax_i)$  we can reuse the old proof otherwise the proof information is invalidated but stored for a later reuse when tackling the proof obligation  $\sigma'(Ax_i)$  by some theorem prover.

*Change in  $M$ :* Since the theory of  $M$  depends on its subgraph, every change in this subgraph may affect the theory of  $M$ . We distinguish two different approaches depending whether  $\Phi_i$  is ProofExists or description of used axioms.

1. In the latter case we know about all used axioms (and their origins). The proof is still valid if all used axioms are still part of the theory of  $M$ . Instead of computing the changes in the axiomatization of  $M$  we check for all triples  $(\tau, K, \Omega)$  whether  $\tau(\Omega)$  is still imported to  $M$  from  $K$  via a morphism  $\tau'$  with  $\tau'(\Omega) = \tau(\Omega)$ .
2. If there is no explicit proof object, we assume that all axioms accessible at the time of the proof have been used for the proof. Thus a proof is invalid if some axiom of a node inside the subgraph of  $M$  has been changed or deleted, or some definition link has been changed or deleted and there is no alternative path with the same morphism. This check is restricted to objects which have existed at the time when the proof was done. Hence each object (links, nodes, axioms, etc.) contains timestamps of its creation, its deletion, or its change. For example, changing a morphism does not affect the validity of a proof if all signature entries which are affected by these changes were introduced after the computation of the proof.

Consider our running example and suppose we had already proven some axioms of STACK mapped as theorems to LIST when we inserted the theory

REL. As REL only adds new axioms to the theory of LIST, all proofs of the axioms are still valid. This holds although the morphism  $\tau$  of the local theorem link from STACK to LIST has changed to  $\tau'$  in order to incorporate the mapping of the new relation  $R$ . In case the local proof object provides the list of used axioms we can easily check that  $\tau(Ax_i) = \tau'(Ax_i)$  holds for all  $1 \leq i \leq n$ . Otherwise, the morphisms  $\tau$  and  $\tau'$  are compared which results in the fact that the only differences between both morphisms concern the mapping of the relation  $R$  which has been introduced after doing the proofs of any  $Ax_i$ . Thus, changing  $\tau$  to  $\tau'$  will not affect the proofs of any  $Ax_i$  done before the insertion of the theory REL.

## 5 Implementation and Related Work

The development graph as well as the techniques for their maintenance are implemented in the MAYA system (cf. [8]). Currently the fixed logic underlying the development graph is higher-order logic. The uniform representation of structured theories in the development graph supports evolutionary formal software development with respect to arbitrary specification languages, provided there exists an adequate mapping from the specification language into development graphs. Currently MAYA integrates parsers for the specification languages CASL (cf. [1]) and VSE-SL (cf. [6]). With respect to the verification in-the-small, MAYA supports the use of arbitrary theorem provers for higher-order logic. To this end a generic interface to propagate the changes of theories to the theorem provers has been implemented. Currently, the HOL-CASL instance of Isabelle/HOL (cf. [3]) and the INKA 5.0 theorem prover (cf. [1]) are integrated into MAYA via this interface. The Lisp sources of MAYA can be obtained from the MAYA-webpage [8].

The KIV system [13] incorporates a development graph similar to the one presented in this paper. However, instead of having basic structuring mechanism like our global and local links, the KIV structure mechanisms are heavy tailored to the structuring constructs of their specification languages. Although this allows for a more adequate representation of global proof obligations, it lacks the ability to easily integrate support for further specification languages. With respect to the verification in-the-large, it also supports the maintenance of established proof obligations when changing the specification, but lacks a mechanism for redundancy checking and elimination. This is due to the absence of decomposition of proof obligations between graphs into proof obligations between the respective subgraphs. With respect to the verification in-the-small, when the specification is changed, the effects on the axioms usable by the theorem prover cannot be determined in an as granular manner as in the MAYA system. Finally, the tight integration of the KIV development graph with the built-in theorem prover hampers the use of further theorem provers.

The SPECWARE system [9] is a formal software design environment. It follows the paradigm of top-down formal software development using refinement, modularization, and parameterization. The whole design and refinement pro-

cess is explicitly represented in some kind of development graph and the arising proof obligations are proven using theorem provers. However, like for the KIV system, the basic structuring mechanisms are tailored to the specification language, which hampers the use of other specification languages. Finally, it lacks the support for redundancy checking and elimination, as well as the maintenance of established proof obligations.

The *Little Theories* approach [7] provides a subset of the theory structuring mechanism of development graphs, i.e. global definition links and proven global theorem links. It is more general than development graph, because each theory (node) can have its own logic, whereas for the current implementation of development graphs presented in this paper, the whole graph is with respect to a single logic. The extension of development graphs to deal with different logics has been achieved in theory in [11]. However, little theories lack on the one hand the ability to represent intermediate states of the development, i.e. a state where there still exist yet unproven postulated global theorem links. On the other hand, there are no mechanisms that exploit the graph structure to reduce the amount of proof obligations and to deal with non-monotonic changes of the theories.

## 6 Conclusion

For the development of industrial-size software systems, the preservation of the structure of specifications is essential not only for the specification of the systems, but also for their verification. Indeed, the structure can be exploited in order to reduce the amount of proof obligations and to support efficiently the revision of specifications, which usually arises in practice.

We presented the implementation of a system for verification in-the-large about structured specifications. It enables to formally find and eliminate redundant proof obligations. Furthermore, it incorporates strategies to transform a proof for some former specification to some new specification, while preserving as many established conjectures as possible.

The theorem proving mechanisms for verification in-the-large are the kernel of the MAYA system [8]. Around that kernel are build on the one hand a uniform interface for parsers of arbitrary specification languages<sup>5</sup>, and on the other hand a uniform interface to use theorem provers for verification in-the-small. These functionalities enable MAYA to bridge the gap between parsers for specification languages and state of the art automated or interactive theorem provers, and deals with all aspects of evolutionary formal software development based on structured specifications.

Future work will consist of extending the verification in-the-large mechanisms to support development graphs with hiding [11]. Further work will also be concerned with the generation of proof-objects for completed developments

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<sup>5</sup> Provided there is an adequate translation of the logic and the structuring constructs of the specification language into the development graph structure.

from MAYA’s internal “in-the-large” proof representation and the annotated “in-the-small” proofs. This proof object shall be used to proof check a completed development, which formally certifies a completed formal software development.

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## A Appendix

*Proof of Lemma 1:* We sketch the proof for global paths which is done by induction on the length of the path:

**Base case:** If the path is empty, then  $\sigma$  is the identity and  $N = M$ . Thus  $\sigma(Th(N)) \subset Th(M)$  holds trivially.

**Step case:** As an induction hypothesis we assume that if  $N \xrightarrow{\sigma'} \Psi_D \uplus \Delta K$  then  $\sigma'(Th(N)) \subset Th(K)$ . Let  $K \xrightarrow{\sigma''} M \in \Psi_D \uplus \Delta$ . Thus,  $\sigma''(Th(K)) \subset Th(M)$  (Def. 3 or Def. 5 resp.) and therefore  $\sigma''(\sigma'(Th(N))) \subset Th(M)$ .  $\square$

*Proof of Theorem 1:*

**Soundness:** We induce on the length  $n$  of the deduction:

**Base case:** let  $n = 1$ . Thus,  $\Delta = \emptyset$  and  $\mathcal{S}$  satisfies  $\Delta$  trivially.

**Step case:** let  $n > 1$  and  $\Psi_D \vdash \Delta'$  be the immediate predecessor of  $\Psi_D \vdash \Delta$ . As induction hypothesis we assume that  $\mathcal{S}$  satisfies  $\Delta'$ . We do a case split according to the applicable rules:

- GD:** Hence  $N \xrightarrow{\sigma} M \in \Delta'$ ,  $K \xrightarrow{\sigma \circ \rho} M \in \Delta'$  for all  $K \xrightarrow{\rho} N \in \Psi_D$  and  $K \xrightarrow{\sigma \circ \rho} M \in \Delta'$  for all  $K \xrightarrow{\rho} N \in \Psi_D$ . Since  $\mathcal{S}$  satisfies  $\Delta'$ , we know that  $\sigma(\Phi_i^N) \subseteq Th(M)$ ,  $\sigma(\rho(Th(K))) \subseteq Th(M)$  for all  $K \xrightarrow{\rho} N \in \Psi_D$  and  $\sigma(\rho(\Phi_i^K)) \subseteq Th(M)$  for all  $K \xrightarrow{\rho} N \in \Psi_D$ . Thus, from the  $\vdash$ -translation property we get
- $$[\sigma(\Phi_i^N) \cup \bigcup_{K \xrightarrow{\rho} N \in \Psi_D} \sigma(\rho(Th(K))) \cup \bigcup_{K \xrightarrow{\rho} N \in \Psi_D} \sigma(\rho(\Phi_i^K))]^{\vdash_{\Sigma^N}} \subseteq Th(M).$$
- Hence,  $\sigma([\Phi_i^N \cup \bigcup_{K \xrightarrow{\rho} N \in \Psi_D} \rho(Th(K)) \cup \bigcup_{K \xrightarrow{\rho} N \in \Psi_D} \rho(\Phi_i^K)]^{\vdash_{\Sigma^M}} \subseteq Th(M)$  holds due to the  $\vdash$ -translation property, i.e.  $\sigma(Th(N)) \subseteq Th(M)$  and thus  $\Psi_D$  satisfies  $N \xrightarrow{\sigma} M$ .
- LD:**  $\sigma(\phi) \in Th(M)$  for all  $\phi \in \Phi_i^N$  implies that  $\Psi_D$  satisfies  $N \xrightarrow{\sigma} M$  and thus  $\mathcal{S}$  satisfies  $\Delta$ .
- GS:** Since  $N \xrightarrow{\sigma} \Psi_D \cup \Delta' M$  holds and  $\mathcal{S}$  satisfies  $\Delta'$  we know that  $\sigma(Th(N)) \subseteq Th(M)$  holds, i.e.  $\mathcal{S}$  satisfies  $N \xrightarrow{\sigma} M$ .
- LS:** Since  $N \xrightarrow{\sigma} \Psi_D \cup \Delta' M$  and  $\mathcal{S}$  satisfies  $\Delta'$  we know that  $\sigma(\Phi_i^N) \subseteq Th(M)$  holds, i.e.  $\mathcal{S}$  satisfies  $N \xrightarrow{\sigma} M$ .

**Completeness:** Suppose,  $\Psi_D$  satisfies  $\Delta$ . Since the development graph  $\mathcal{S} = \langle \mathcal{N}, \Psi_D \uplus \Psi_T \rangle$  is acyclic with respect to  $\Psi_D$  we define the *depth* of a node  $N \in \mathcal{N}$  as the length of the longest path of links in  $\Psi_D$  from some leaf node in  $\mathcal{N}$  to  $N$ . We induce on the multiset  $depths(\Delta)$  of depths of the source nodes in  $\Delta$ .

**Base case:** If  $depths(\Delta) = \emptyset$  then  $\Delta = \emptyset$  and  $\Psi_D \vdash \emptyset$  holds by rule AX.

**Induction step:** Let  $depths(\Delta) \neq \emptyset$ . As an induction hypothesis suppose the conjecture holds for all  $\Delta'$  which are smaller than  $\Delta$  with respect to the multiset-ordering on  $depths$ .

- Let  $N \xrightarrow{\sigma} M \in \Delta$  with  $depth(N) = \max(depths(\Delta))$ . Since  $\Psi_D$  satisfies  $\Delta - \{N \xrightarrow{\sigma} M\}$ , applying the induction hypothesis yields  $\Psi \vdash \Delta - \{N \xrightarrow{\sigma} M\}$ . As  $\Psi_D$  satisfies  $N \xrightarrow{\sigma} M$ , we know that  $\sigma(\Phi_i^N) \subseteq Th(M)$  holds and apply rule LD to deduce finally  $\Psi_D \vdash \Delta$ .
- Let  $N \xrightarrow{\sigma} M \in \Delta$  with  $depth(N) = \max(depths(\Delta))$ . For all links  $K \xrightarrow{\rho} N \in \Psi_D$   $\sigma(\rho(Th(K))) \subseteq Th(M)$  holds. Analogously for all links  $K \xrightarrow{\rho} N \in \Psi_D$  holds  $\sigma(\rho(\Phi_i^K)) \subseteq Th(M)$ . Thus,  $\mathcal{S}$  satisfies both  $\bigcup_{K \xrightarrow{\rho} N \in \Psi_D} \{K \xrightarrow{\sigma \circ \rho} M\}$  and  $\bigcup_{K \xrightarrow{\rho} N \in \Psi_D} \{K \xrightarrow{\sigma \circ \rho} M\}$ . Applying the induction hypothesis yields  $\Psi_D \vdash (\Delta - \{N \xrightarrow{\sigma} M\}) \cup \bigcup_{K \xrightarrow{\rho} N \in \Psi_D} \{K \xrightarrow{\sigma \circ \rho} M\} \cup \bigcup_{K \xrightarrow{\rho} N \in \Psi_D} \{K \xrightarrow{\sigma \circ \rho} M\}$ . Since  $\mathcal{S}$  satisfies also  $N \xrightarrow{\sigma} M$  we use the argumentation of the first case to deduce  $\Psi_D \vdash (\Delta - \{N \xrightarrow{\sigma} M\}) \cup \{N \xrightarrow{\sigma} M\} \cup \bigcup_{K \xrightarrow{\rho} N \in \Psi_D} \{K \xrightarrow{\sigma \circ \rho} M\} \cup \bigcup_{K \xrightarrow{\rho} N \in \Psi_D} \{K \xrightarrow{\sigma \circ \rho} M\}$  and apply rule GD to derive  $\Psi_D \vdash \Delta$ .  $\square$