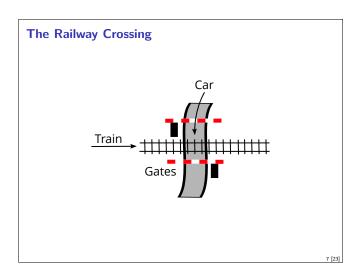


Where are we?

- ▶ Lecture 1: Concepts of Quality
- ► Lecture 2: Concepts of Safety and Security, Norms and Standards
- ► Lecture 3: Quality of the Software Development Process
- ► Lecture 4: Requirements Analysis
- Lecture 5: High-Level Design & Formal Modelling
- ► Lecture 6: Detailed Specification, Refinement & Implementation
- ► Lecture 7: Testing
- ► Lecture 8: Program Analysis
- ► Lecture 9: Verification with Floyd-Hoare Logic
- ► Lecture 10: Verification Condition Generation
- ▶ Lecture 11: Model-Checking with LTL and CTL
- ▶ Lecture 12: NuSMV and Spin
- ► Lecture 13: Conclusions

The Model-Checking Problem				
The Basic Question				
Given a model $\mathcal{M},$ and a property $\phi,$ we want to know whether				
$\mathcal{M} \models \phi$				
► What is <i>M</i> ? Finite state machines				
► What is <i>φ</i> ? Temporal logic				
How to prove it? Enumerating states — model checking				

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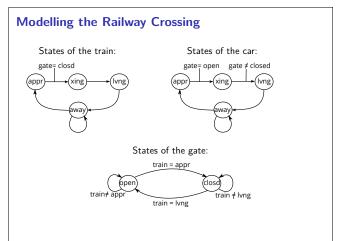
Organisatorisches

- ► Noch ein Übungsblatt?
- Prüfungen KW 06 (4./5. Feb.)

Introduction

- Last lectures: verifying program properties with the Floyd-Hoare calculus
- In the Floyd-Hoare calculus, program verification is reduced to a deductive problem by translating the program into logic (specifically, state change becomes substitution).
- Model-checking takes a different approach: the system is modelled directly by a finite-state machine, and properties are expressed in some logic for FSM. Program verification reduces to state enumeration, which can be done automatically.
- The logics we will considere here are temporal logic: linear temporal logic (LTL) and branching temporal logic (CTL)

Finite State Machines Finite State Machine (FSM) A FSM is given by M = (Σ, →) where Σ is a finite set of states, and → ⊆ Σ × Σ is a transition relation, such that → is left-total: ∀s ∈ Σ. ∃s' ∈ Σ. s → s' Many variations of this definition exists, e.g. sometimes we have state variables or labelled transitions. Note there is no final state, and no input or output (this is the key difference to automata). If → is a function, the FSM is deterministic, otherwise it is non-deterministic.



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The FSM

The states here are a map from variables Car, Train, Gate to the domains

$$\begin{array}{lll} \Sigma_{Car} &=& \{ \textit{appr}, \textit{xing}, \textit{lvng}, \textit{away} \} \\ \Sigma_{\textit{Train}} &=& \{ \textit{appr}, \textit{xing}, \textit{lvng}, \textit{away} \} \end{array}$$

$$\Sigma_{Gate} = \{open, clsd\}$$

or alternatively, a three-tuple $S \in \Sigma = \Sigma_{\mathit{Car}} \times \Sigma_{\mathit{Train}} \times \Sigma_{\mathit{Gate}}.$

The transition relation is given by e.g.

 $\begin{array}{l} \langle \textit{away},\textit{open},\textit{away} \rangle \rightarrow \langle \textit{appr},\textit{open},\textit{away} \rangle \\ \langle \textit{appr},\textit{open},\textit{away} \rangle \rightarrow \langle \textit{xing},\textit{open},\textit{away} \rangle \end{array}$

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Linear Temporal Logic (LTL) and Paths

- LTL allows us to talk about paths in a FSM, where a path is a sequence of states connected by the transition relation.
- We first define the syntax of formula,
- then what it means for a path to satisfy the formula, and
- from that we derive the notion of a model for an LTL formula.

Paths

Given a FSM $\mathcal{M} = \langle \Sigma, \rightarrow \rangle$, a **path** in \mathcal{M} is an (infinite) sequence $\langle s_1, s_2, s_3, \ldots \rangle$ such that $s_i \in \Sigma$ and $s_i \rightarrow s_{i+1}$ for all *i*.

• For a path $p = \langle s_1, s_2, s_3, \ldots \rangle$, we write p_i for s_i (selection) and p^i for $\langle s_i, s_{i+1}, \ldots \rangle$ (the suffix starting at *i*).

Satifsaction and Models of LTL

Given a path p and an LTL formula $\phi,$ the satisfaction relation $p\models\phi$ is defined inductively as follows:

 $p \models \phi \land \psi \text{ iff } p \models \phi \text{ and } p \models \psi$ р ⊨ True $p \models \phi \lor \psi$ iff $p \models \phi$ or $p \models \psi$ р ⊭ False $\models p \text{ iff } p(p_1) \qquad p \models \phi \longrightarrow \psi \text{ iff whenever } p \models \phi \text{ then } p \models \psi$ р $\neg \phi$ iff $p \not\models \phi$ р Þ $\models X \phi \text{ iff } p^2 \models \phi$ р р \models G ϕ iff for all *i*, we have $p^i \models \phi$ $\models \mathsf{F}\phi \text{ iff there is } i \text{ such that } p^i \models \phi$ р $\models \phi U \psi$ iff there is $i p^i \models \psi$ and for all $j = 1, \dots, i - 1, p^j \models \phi$ p Models of LTL formulae A FSM \mathcal{M} satisfies an LTL formula ϕ , $\mathcal{M} \models \phi$, iff every path p in \mathcal{M} satisfies ϕ .

Computational Tree Logic (CTL) LTL does not allow us the quantify over paths, e.g. assert the existance of a path satisfying a particular property. To a limited degree, we can solve this problem by negation: instead of asserting a property φ, we check wether ¬φ is satisfied; if that is not the case, φ holds. But this does not work for mixtures of universal and existential quantifiers. Computational Tree Logic (CTL) is an extension of LTL which allows this by adding universal and existential quantifiers to the modal operators.

The name comes from considering paths in the computational tree obtained by unwinding the FSM.

Railway Crossing — Safety Properties

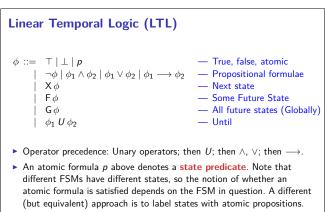
- Now we want to express safety (or security) properties, such as the following:
- Cars and trains never cross at the same time.
- The car can always leave the crossing
- Approaching trains may eventually cross.
- There are cars crossing the tracks.
- We distinguish safety properties from liveness properties:
- Safety: something bad never happens.
- Liveness: something good will (eventually) happen.
- $\blacktriangleright\,$ To express these properties, we need to talk about sequences of states in an FSM.

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 \blacktriangleright From these, we can define other operators, such as $\phi~R~\psi$ (release) or $\phi~W~\psi$ (weak until).

The Railway Crossing Cars and trains never cross at the same time. G¬(car = xing ∧ train = xing) A car can always leave the crossing: G(car = xing → F(car = lvng)) Approaching trains may eventually cross: G(train = appr → F(train = xing)) There are cars crossing the tracks: F(car = xing) means something else! Can not express this in LTL!

CTL Formulae	
$\begin{split} \phi &::= & \top \mid \perp \mid p \\ & \mid \neg \phi \mid \phi_1 \land \phi_2 \mid \phi_1 \lor \phi_2 \mid \phi_1 \longrightarrow \phi_2 \\ & \mid AX \phi \mid EX \phi \\ & \mid AF \phi \mid EF \phi \\ & \mid AG \phi \mid EG \phi \\ & \mid A[\phi_1 \ U \ \phi_2] \mid E[\phi_1 \ U \ \phi_2] \end{split}$	 True, false, atomic Propositional formulae All or some next state All or some future states All or some global future Until all or some

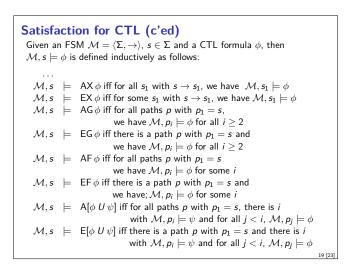
Satifsfaction

- ▶ Note that CTL formulae can be considered to be a LTL formulae with a 'modality' (*A* or *E*) added on top of each temporal operator.
- ▶ Generally speaking, the *A* modality says the temporal operator holds for all paths, and the *E* modality says the temporal operator only holds for all least one path.
 - Of course, that strictly speaking is not true, because the arguments of the temporal operators are in turn CTL forumulae, so we need recursion.
- This all explains why we do not define a satisfaction for a single path p, but satisfaction with respect to a specific state in an FSM.

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LTL and CTL

- ▶ We have seen that CTL is more expressive than LTL, but (surprisingly), there are properties which we can formalise in LTL but not in CTL!
- ▶ Example: all paths which have a *p* along them also have a *q* along them.
- ▶ LTL: $F p \longrightarrow F q$
- CTL: Not AF p → AF q (would mean: if all paths have p, then all paths have q), neither AG(p → AF q) (which means: if there is a p, it will be followed by a q).
- ► The logic CTL* combines both LTL and CTL (but we will not consider it further here).

Summary

- Model-checking allows us to show to show properties of systems by enumerating the system's states, by modelling systems as finite state machines, and expressing properties in temporal logic.
- We considered Linear Temporal Logic (LTL) and Computational Tree Logic (CTL). LTL allows us to express properties of single paths, CTL allows quantifications over all possible paths of an FSM.
- The basic problem: the system state can quickly get huge, and the basic complexity of the problem is horrendous. Use of abstraction and state compression techniques make model-checking bearable.
- Next lecture: practical experiments with model-checkers (NuSMV and/or Spin)

Satisfaction for CTL

Given an FSM $\mathcal{M} = \langle \Sigma, \rightarrow \rangle$, $s \in \Sigma$ and a CTL formula ϕ , then $\mathcal{M}, s \models \phi$ is defined inductively as follows:

\mathcal{M}, s	F	True
\mathcal{M}, s	¥	False
\mathcal{M}, s	F	p iff p(s)
\mathcal{M}, s	F	$\phi \land \psi \text{ iff } \mathcal{M}, s \models \phi \text{ and } \mathcal{M}, s \models \psi$
\mathcal{M}, s	Þ	$\phi \lor \psi$ iff $\mathcal{M}, s \models \phi$ or $\mathcal{M}, s \models \psi$
\mathcal{M}, s	F	$\phi \longrightarrow \psi$ iff whenever $\mathcal{M}, s \models \phi$ then $\mathcal{M}, s \models \psi$

Patterns of Specification

- Something bad (p) cannot happen: AG ¬p
- p occurs infinitly often: AG(AF p)
- \blacktriangleright p occurs eventually: AF p
- In the future, p will hold eventually forever: AF AG p
- \blacktriangleright Whenever p will hold in the future, q will hold eventually: AG($p \longrightarrow$ AF q)
- In all states, p is always possible: AG(EF p)

State Explosion and Complexity

- The basic problem of model checking is state explosion.
- Even our small railway crossing has
- $$\begin{split} |\Sigma| &= |\Sigma_{Car} \times \Sigma_{Train} \times \Sigma_{Gate}| = |\Sigma_{Car}| \cdot |\Sigma_{Train}| \cdot |\Sigma_{Gate}| = 4 \cdot 4 \cdot 2 = 32 \\ \text{states. Add one integer variable with } 2^{32} \text{ states, and this gets} \\ \text{intractable.} \end{split}$$

Theoretically, there is not much hope. The basic problem of deciding wether a particular formula holds is known as the satisfiability problem, and for the temporal logics we have seen, its complexity is as follows:

- ► LTL without *U* is *NP*-complete.
- LTL is PSPACE-complete.
- CTL is EXPTIME-complete.
- The good news is that at least it is decidable. Practically, state abstraction is the key technique. E.g. instead of considering all possible integer values, consider only wether *i* is zero or larger than zero.

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