

Timed CSP

Syntax and semantics of Timed CSP according to Steve Schneider, Concurrent and Real-time Systems, The CSP Approach, John Wiley & Sons, Ltd, 2000.

1 Syntax

$P ::=$	STOP	deadlock
	SKIP	successful termination
	$c!e \rightarrow P$	communication (output)
	$c?e \rightarrow P$	communication (input)
	$a \rightarrow P$	event prefix
	$a : A \rightarrow P$	prefix choice
	$a \xrightarrow{t} P$	timed prefix
	$P \stackrel{t}{\triangleright} P$	timeout
	$P \square P$	external choice
	$P \sqcap P$	internal choice
	$\mu X \bullet P$	recursion
	$P \parallel_A B P$	alphabetized parallel
	$P P$	interleaving
	$P \parallel_A P$	synchronized parallel
	$P \setminus A$	hiding
	$f(P)$	renaming
	$f^{-1}(P)$	backward renaming
	$P ; P$	sequential composition
	$P \triangle P$	interrupt
	$P \triangle_t P$	timed interrupt
	$Wait\ t$	delay
	X	process reference
	(P)	brackets

2 Operational Semantics

Deadlock (DL)

(DL1)

$$\frac{}{\text{STOP} \xrightarrow{d} \text{STOP}}$$

Successful Termination (T)

(T1)

$$\frac{}{\text{SKIP} \xrightarrow{d} \text{SKIP}}$$

(T2)

$$\frac{}{\text{SKIP} \xrightarrow{\checkmark} \text{STOP}}$$

Event Prefixing (PR)

(PR1)

$$\frac{}{(a \rightarrow P) \xrightarrow{d} (a \rightarrow P)}$$

(PR2)

$$\frac{}{(a \rightarrow P) \xrightarrow{a} P}$$

Prefix Choice (PC)

(PC1)

$$\frac{}{x : A \rightarrow P(x) \xrightarrow{d} x : A \rightarrow P(x)}$$

(PC2)

$$\frac{}{x : A \rightarrow P(x) \xrightarrow{a} P(a)} \quad [a \in A]$$

Timed Event Prefix (TPR)

(TPR1)

$$\frac{}{(a@u \rightarrow Q) \xrightarrow{d} (a@u \rightarrow Q[(u+d)/u])}$$

(TPR2)

$$\frac{}{(a@u \rightarrow Q) \xrightarrow{a} Q[0/u]}$$

Timeout (TO)

(TO1)

$$\frac{P \xrightarrow{d'} P'}{P \triangleright^d Q \xrightarrow{d'} P' \triangleright^{d-d'} Q} \quad [0 < d' \leq d]$$

(TO2)

$$\frac{P \xrightarrow{\mu} P'}{P \triangleright^d Q \xrightarrow{\mu} P'} \quad [\mu \neq \tau]$$

(TO3)

$$\frac{P \xrightarrow{\tau} P'}{P \triangleright^d Q \xrightarrow{\tau} P' \triangleright^d Q}$$

(TO4)

$$\frac{}{P \triangleright^0 Q \xrightarrow{\tau} Q}$$

External Choice (EC)

(EC1)

$$\frac{P \xrightarrow{d} P' \quad Q \xrightarrow{d} Q'}{P \sqcap Q \xrightarrow{d} P' \sqcap Q'}$$

(EC2)

$$\frac{P \xrightarrow{\tau} P'}{P \sqcap Q \xrightarrow{\tau} P' \sqcap Q \quad Q \sqcap P \xrightarrow{\tau} Q \sqcap P'}$$

(EC3)

$$\frac{P \xrightarrow{\mu} P'}{P \sqcap Q \xrightarrow{\mu} P' \quad Q \sqcap P \xrightarrow{\mu} P'} \quad [\mu \neq \tau]$$

Internal Choice (IC)

(IC1)

$$\frac{}{P \sqcap Q \xrightarrow{\tau} P \quad P \sqcap Q \xrightarrow{\tau} Q}$$

(Mutual) Recursion (REC)

In these rules, σ represents the syntactic environment of the process N (which is called "surrounding context of process definitions" by Schneider). In contrast to earlier operational semantics definitions for Timed CSP, recursion unfolding does not produce an internal event in Schneider's definition.

(REC1)

$$\frac{P \xrightarrow{d} P'}{N \xrightarrow{d} P'} \quad [(N = P) \in \sigma]$$

(REC2)

$$\frac{P \xrightarrow{\mu} P'}{N \xrightarrow{\mu} P'} \quad [(N = P) \in \sigma]$$

Alphabetized Parallel (AP)

(AP1)

$$\frac{P \xrightarrow{t} P' \quad Q \xrightarrow{t} Q'}{P \parallel_A Q \xrightarrow{t} P' \parallel_A Q'}$$

(AP2)

$$\frac{P \xrightarrow{\mu} P'}{P \parallel_A Q \xrightarrow{\mu} P' \parallel_A Q} \quad [\mu \in (A \cup \{\tau\}) \setminus B]$$

(AP3)

$$\frac{Q \xrightarrow{\mu} Q'}{P \parallel_A Q \xrightarrow{\mu} P \parallel_A Q'} \quad [\mu \in (B \cup \{\tau\}) \setminus A]$$

(AP4)

$$\frac{P \xrightarrow{\mu} P' \quad Q \xrightarrow{\mu} Q'}{P \parallel_A Q \xrightarrow{\mu} P' \parallel_A Q'} \quad [\mu \in (A \cap B)^\vee]$$

Interleaving (IL)

(IL1)

$$\frac{P \overset{t}{\rightsquigarrow} P' \quad Q \overset{t}{\rightsquigarrow} Q'}{P \parallel Q \overset{t}{\rightsquigarrow} P' \parallel Q'}$$

(IL2)

$$\frac{P \xrightarrow{\mu} P'}{P \parallel Q \xrightarrow{\mu} P' \parallel Q \quad Q \parallel P \xrightarrow{\mu} Q \parallel P'} \quad [\mu \neq \checkmark]$$

(IL3)

$$\frac{P \xrightarrow{\checkmark} P' \quad Q \xrightarrow{\checkmark} Q'}{P \parallel Q \xrightarrow{\checkmark} P' \parallel Q'}$$

Interface Parallel (IP)

(IP1)

$$\frac{P \overset{t}{\rightsquigarrow} P' \quad Q \overset{t}{\rightsquigarrow} Q'}{P \parallel_A Q \overset{t}{\rightsquigarrow} P' \parallel_A Q'}$$

(IP2)

$$\frac{P \xrightarrow{\mu} P'}{P \parallel_A Q \xrightarrow{\mu} P' \parallel_A Q \quad Q \parallel_A P \xrightarrow{\mu} Q \parallel_A P'} \quad [\mu \notin A]$$

(IP3)

$$\frac{P \xrightarrow{\mu} P' \quad Q \xrightarrow{\mu} Q'}{P \parallel_A Q \xrightarrow{\mu} P' \parallel_A Q'} \quad [\mu \in A^\checkmark]$$

Hiding (H)

(H1)

$$\frac{P \overset{d}{\rightsquigarrow} P' \quad \forall a \in A \bullet \neg(P \xrightarrow{a})}{P \setminus A \overset{d}{\rightsquigarrow} P' \setminus A}$$

(H2)

$$\frac{P \xrightarrow{\mu} P'}{P \setminus A \xrightarrow{\tau} P' \setminus A} \quad [\mu \in A]$$

(H3)

$$\frac{P \xrightarrow{\mu} P'}{P \setminus A \xrightarrow{\mu} P' \setminus A} \quad [\mu \notin A]$$

Event Renaming (ER)

(ER1)

$$\frac{P \overset{d}{\rightsquigarrow} P'}{f(P) \overset{d}{\rightsquigarrow} f(P')}$$

(ER2)

$$\frac{P \xrightarrow{\mu} P'}{f(P) \xrightarrow{f(\mu)} f(P')} \quad [\mu \neq \tau]$$

(ER3)

$$\frac{P \xrightarrow{\tau} P'}{f(P) \xrightarrow{\tau} f(P')}$$

Backward Renaming (BR)

(BR1)

$$\frac{P \xrightarrow{d} P'}{f^{-1}(P) \xrightarrow{d} f^{-1}(P')}$$

(BR2)

$$\frac{P \xrightarrow{f^{-1}(\mu)} P'}{f^{-1}(P) \xrightarrow{\mu} f^{-1}(P')} \quad [\mu \neq \tau]$$

(BR3)

$$\frac{P \xrightarrow{\tau} P'}{f^{-1}(P) \xrightarrow{\tau} f^{-1}(P')}$$

Sequential Composition (SC)

(SC1)

$$\frac{P \xrightarrow{d} P' \quad \neg(P \xrightarrow{\checkmark})}{P ; Q \xrightarrow{d} P' ; Q}$$

(SC2)

$$\frac{P \xrightarrow{\mu} P'}{P ; Q \xrightarrow{\mu} P' ; Q} \quad [\mu \neq \checkmark]$$

(SC3)

$$\frac{P \xrightarrow{\checkmark} P'}{P ; Q \xrightarrow{\tau} Q}$$

Interrupt (IR)

(IR1)

$$\frac{P \xrightarrow{\mu} P'}{P \triangle Q \xrightarrow{\mu} P' \triangle Q} \quad [\mu \neq \checkmark]$$

(IR2)

$$\frac{P \xrightarrow{\checkmark} P'}{P \triangle Q \xrightarrow{\checkmark} P'}$$

(IR3)

$$\frac{Q \xrightarrow{\tau} Q'}{P \triangle Q \xrightarrow{\tau} P \triangle Q'}$$

(IR4)

$$\frac{Q \xrightarrow{a} Q'}{P \triangle Q \xrightarrow{a} Q'}$$

(IR5)

$$\frac{P \xrightarrow{d} P' \quad Q \xrightarrow{d} Q'}{P \triangle Q \xrightarrow{d} P' \triangle Q'}$$

Timed Interrupt (TIR)

(TIR1)

$$\frac{P \xrightarrow{\mu} P'}{P \triangle_d Q \xrightarrow{\mu} P' \triangle_d Q} \quad [\mu \neq \checkmark]$$

(TIR2)

$$\frac{P \xrightarrow{\checkmark} P'}{P \triangle_d Q \xrightarrow{\checkmark} P'}$$

(TIR3)

$$\frac{}{P \triangle_0 Q \xrightarrow{\tau} Q}$$

(TIR4)

$$\frac{P \overset{d'}{\rightsquigarrow} P'}{P \triangle_d Q \overset{d'}{\rightsquigarrow} P' \triangle_{d-d'} Q} \quad [d' \leq d]$$

Delay (D)

(D1)

$$\frac{}{\text{WAIT } u \overset{d}{\rightsquigarrow} \text{WAIT } (u - d)} \quad [d \leq u]$$

(D2)

$$\frac{}{\text{WAIT } 0 \xrightarrow{\tau} \text{SKIP}}$$

3 Labelled Transition Systems and Time

Recall the standard definition for *Labelled Transition Systems (LTS)*:

Definition 1 (Labelled Transition System (LTS)) An LTS is a tuple (S, E, T, s_0) , where S is a set of states, E a set of labels, $T \subseteq S \times E \times S$ the transition relation and s_0 the initial state. \square

For describing the operational semantics of a TCSP process, a (timed) LTS (S, E, T, s_0) is used, where the state S contains process references or TCSP terms. The set of labels is defined as $E = \Sigma \cup \{\checkmark, \tau\} \cup \mathbb{R}_0^+$, with $(\Sigma \cup \{\checkmark, \tau\}) \cap \mathbb{R}_0^+ = \emptyset$. The subset $\Sigma \subset E$ is called the *alphabet* of the process. Transitions in T are defined by axioms which are written as inference rules in the usual SOS-style (structured operational semantics) of Plotkin.

Transitions carrying labels $\mu \in \Sigma \cup \{\checkmark, \tau\}$ are written $P \xrightarrow{\mu} P'$ and called *event transitions*. They are interpreted as instantaneous events performed by processes P and accompanied by a state change to a new process P' . If $\mu \in \Sigma$, it is interpreted as an observable event, while $\mu = \tau$ denotes an internal event which cannot be identified by a (testing) environment. Event \checkmark signifies process termination. TCSP follows the principle of *maximal progress*: Events occur immediately if all participants are willing to engage in it. TCSP supports the specification of *non-deterministic* behaviour: If several transition may fire in a given state, one of them is selected in an arbitrary way. This transition may disable the other ones which have been simultaneously enabled. As a consequence, maximal progress does not guarantee that enabled events really occur in a situation where several transition rules apply. However, the principle of *maximal parallelism* guarantees that events which are simultaneously enabled by parallel processes all happen at the same time (via consecutive application of the associated transition rules and since event transitions are instantaneous), if previous transitions do not disable the following ones.

TCSP is based on *Newtonian time*, assuming the existence of a conceptual global clock. Time is dense; the possible values of time ticks are modelled by \mathbb{R}_0^+ . Transitions with labels $d \in \mathbb{R}_0^+$ are called *evolution transitions* and written as $P \overset{d}{\rightsquigarrow} P'$. They specify how processes P evolve over time d to new process states P' , without producing any visible events.

Since event transitions occur instantaneously, precautions are necessary to avoid the specification of unrealistic processes producing infinitely many events within a bounded – or even zero-length – time interval. To this end, the concept of *time-guarded* TCSP terms has been introduced by Schneider, which basically requires that in every non-terminating recursive system of TCSP processes timing operators exist which guarantee that minimal waiting times occur between sequences of finitely many events. Time-guardedness ensures *finite variability*: In any bounded time interval, Processes can generate only finite numbers of events.