

Coverage of OCL Operation Specifications and Invariants^{*}

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Abstract. We consider operation coverage of OCL operation specifications and invariants in class diagrams with respect to sequence diagrams. The coverage criteria are based on the operations that are executed from the sequence diagrams and their asserted OCL subexpressions. We propose an algorithm that automatically generates a set of sequence diagrams in order to maximise these coverage criteria. A model finder is leveraged for this purpose. As a result, also operations and constraints can be determined that can never be executed and asserted, respectively. Our algorithm has been implemented in the UML specification tool USE.

1 Introduction

Given a class diagram with OCL operation specifications, invariants and a set of sequence diagrams, we define two coverage metrics that measure (1) how many operations of the class diagram have been called and (2) how many OCL subexpressions evaluated to true for this purpose. We define the coverage semantics on top of the precise modelling approach that has been presented by Mark Richters in [11]. As a result, the coverage metrics can readily be integrated in the context of formal analysis tools.

We demonstrate this by utilising model finders for behavioural modelling tasks to automatically generate sequence diagrams to increase coverage with respect to the defined metrics. Since model finders traverse the complete search space (and often efficiently), also “dead” operations or “dead” subexpressions can be found with our algorithm. Analogously to “dead code” in software development, these refer to operations that can never be called or subexpressions that never evaluate to true.

We have integrated the coverage metrics in the UML specification tool USE [6]. When starting the program with a class diagram and some initial sequence diagrams, the initial coverage is reported. The user of the tool can then start the

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model finder to generate new sequence diagrams which successively increase the coverage or pinpoint the user to “dead” operations or subexpressions.

Coverage metrics for modelling languages have been considered in the past, but rarely have methods been provided to automatically increase the proposed coverage criteria. In [12] coverage criteria based on the execution traces of sequence diagrams have been defined, but no algorithm has been provided that generates input data to increase the coverage. An approach very similar to ours has been proposed in [17] where model finders are exercised in order to find sequence diagrams that adhere to a given specification. However, coverage has not been considered in this context.

The remainder of the paper is structured as follows. The next section reviews class diagrams, system states, and model finding and introduces the formal notation that is used as a basis in the paper. Section 3 proposes two coverage criteria and in Sect. 4 it is described how model finding can be utilised in order to automatically find sequence diagrams to increase the coverage of a class diagram. The implementation in USE is illustrated in Sect. 5 before related work is discussed in Sect. 6. Section 7 concludes the paper.

2 Preliminaries

This section introduces a notation that is used to describe class diagrams and system states in the remainder of the paper. Also, model finding is reviewed.

2.1 Class Diagrams and System States

We are making use of the notation that has been introduced in [11]. Associations have no immediate influence on our proposed coverage metric and therefore we use simpler definitions that omit details on associations.

Definition 1 (Class diagram). *A class diagram is denoted as*

$$\mathcal{M} = (\text{CLASS}, \text{ATT}_c, \text{OP}_c, \prec) , \quad (1)$$

where

- CLASS is a finite set of class names,
- ATT_c are sets of attributes for each class $c \in \text{CLASS}$ defined as signatures $a : t_c \rightarrow t$, where a is the attribute name, t_c is the type of class c , and t is the attribute type,
- OP_c are sets of operations for each class $c \in \text{CLASS}$ defined as signatures $\omega : t_c \times t_1 \times \dots \times t_n \rightarrow t$, where ω is the operation name, t_c is the type of class c , t_1, \dots, t_n are the types of the operation’s n parameters, and t is the operation’s return type,
- and \prec is a partial order on CLASS to reflect the generalisation hierarchy.

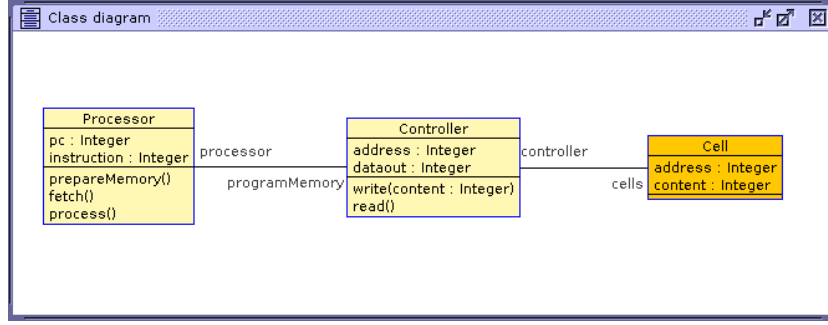


Fig. 1. Class diagram

We define

$$\text{ops}(\mathcal{M}) \stackrel{\text{def}}{=} \bigcup_{c \in \text{CLASS}} \text{OP}_c . \quad (2)$$

For ATT_c and OP_c their reflexive closures

$$\begin{aligned} \text{ATT}_c^* &\stackrel{\text{def}}{=} \text{ATT}_c \cup \bigcup_{c' \in \text{parents}(c)} \text{ATT}_{c'} \quad \text{and} \\ \text{OP}_c^* &\stackrel{\text{def}}{=} \text{OP}_c \cup \bigcup_{c' \in \text{parents}(c)} \text{OP}_{c'} \end{aligned} \quad (3)$$

are defined with $\text{parents}(c) \stackrel{\text{def}}{=} \{c' \mid c' \in \text{CLASS} \wedge c \prec c'\}$.

Example 1. Figure 1 shows a class diagram that will serve as running example throughout the paper. It models the memory access part of a processor architecture. The processor which has a program counter and a current instruction is connected to a memory controller which offers operations to read and write to memory which is represented in terms of cells.³ The precise formal notation of the class diagram is

$$\text{CLASS} = \{ \text{Processor}, \text{Controller}, \text{Cell} \}$$

$$\text{ATT}_{\text{Processor}} = \{ \text{pc} : \text{Processor} \rightarrow \text{Integer}, \text{instruction} : \text{Processor} \rightarrow \text{Integer} \}$$

$$\text{ATT}_{\text{Controller}} = \{ \text{address} : \text{Controller} \rightarrow \text{Integer}, \text{dataout} : \text{Controller} \rightarrow \text{Integer} \}$$

$$\text{ATT}_{\text{Cell}} = \{ \text{address} : \text{Cell} \rightarrow \text{Integer}, \text{content} : \text{Cell} \rightarrow \text{Integer} \}$$

³ The class diagram can be downloaded as a model for USE at www.informatik.uni-bremen.de/agra/files/memory.use

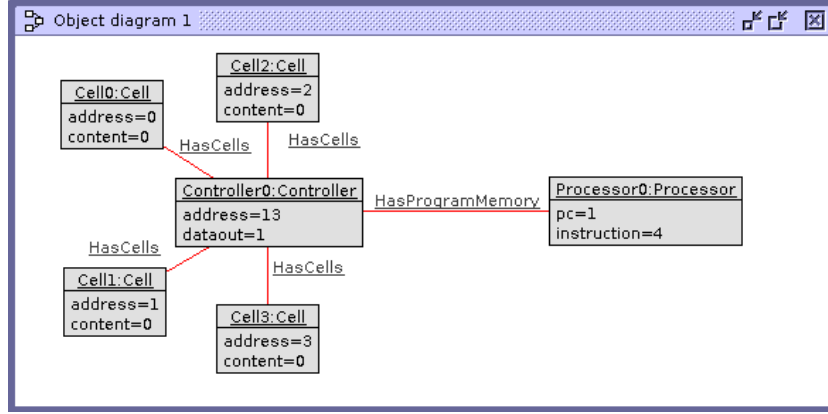


Fig. 2. System state

$$OP_{Processor} = \{ \text{prepareMemory} : Processor \rightarrow VoidType, \\ \text{fetch} : Processor \rightarrow VoidType, \\ \text{process} : Processor \rightarrow VoidType \}$$

$$OP_{Controller} = \{ \text{write} : Controller \times Integer \rightarrow VoidType, \\ \text{read} : Controller \rightarrow VoidType \} .$$

Definition 2 (Class domain). The set of object identifiers of a class $c \in \text{CLASS}$ is given by an infinite set $\text{oid}(c) = \{c_1, c_2, \dots\}$. Then, the domain of c is defined as

$$I_{\text{CLASS}}(c) \stackrel{\text{def}}{=} \bigcup_{\substack{c' \in \text{CLASS} \\ c' \preceq c}} \{\text{oid}(c')\} . \quad (4)$$

In general, we will use the letter I to denote an interpretation mapping [11] that defines the semantics of OCL expressions.

Definition 3 (System state). A system state for a class diagram \mathcal{M} is a structure

$$\sigma(\mathcal{M}) = (\sigma_{\text{CLASS}}, \sigma_{\text{ATT}}) , \quad (5)$$

with

- finite sets $\sigma_{\text{CLASS}}(c) \subset \text{oid}(c)$ containing all objects of class $c \in \text{CLASS}$ in the system state and
- functions $\sigma_{\text{ATT}}(a) : \sigma_{\text{CLASS}}(c) \rightarrow I(t)$ for each $a : t_c \rightarrow t \in \text{ATT}_c^*$.

$I(t)$ is an interpretation function for variables of types t .

If the context is clear, \mathcal{M} can be omitted and a system state is simply written as σ .

Example 2. A valid system state for the class diagram in Fig. 1 is shown in Fig. 2.

Class diagrams can be accompanied by expressions in the object constraint language (OCL, [15]) that is part of the UML standard. OCL allows the specification of formal constraints in the context of a model. Since constraints are conditions on all system states and transitions between states, a set of constraints therefore restricts the set of valid system states. In the extreme case, the set of possible system states is empty; in this case the model is called *inconsistent*. Constraints are primarily used to express invariants which are global constraints that hold in every system state and operation specifications in terms of pre- and postconditions that are evaluated locally in the context of an operation call. An operation can only be called if the preconditions hold and must ensure that after execution its postconditions evaluate to true. In general an OCL expression that evaluates to a value of type t is an element of the set Expr_t . The following definitions provide notation for invariants and operation specifications.

Definition 4 (Invariants). *All invariants of a class c are contained in the set $\mathcal{I}(c) \subset \text{Expr}_{\text{Boolean}}$. All these Boolean OCL expressions contain a variable `self` that is of type c . All invariants of a model are denoted*

$$\mathcal{I}(\mathcal{M}) \stackrel{\text{def}}{=} \bigcup_{c \in \text{CLASS}} \mathcal{I}(c) , \quad (6)$$

and as in the notation for system states the \mathcal{M} can be omitted, i.e., we write \mathcal{I} , if the use is clear from the context.

Example 3. One invariant for the memory controller model from Fig. 1 is:

```
context Controller
inv uniqueCells: cells->forAll(c1, c2 |
    c1 <> c2 implies c1.address <> c2.address)
```

This invariant states that the cells that are associated to a memory controller must have a unique address.

Definition 5 (Pre- and postconditions). *Given an operation $\omega \in \text{ops}(\mathcal{M})$, the sets $\triangleleft(\omega) \subseteq \text{Expr}_{\text{Boolean}}$ and $\triangleright(\omega) \subseteq \text{Expr}_{\text{Boolean}}$ are the pre- and postconditions of ω . The notation is borrowed from [13].*

Example 4. The operation specification for the operation *write* of class *Controller* is:

```
context Controller::write(content: Integer)
pre: address < 10
pre: content < 4
post: cells->one(c | c.address = address and c.content = content)
post: cells->forAll(c | c.address = c.address@pre)
post: cells->forAll(c | c.address <> address implies
    c.content = c.content@pre)
```

The two preconditions ensure that a valid address and content have been assigned to the attributes of the *Controller* object. The first postcondition ensures that the cell at the given address has the new content after the operation has been called. The second postcondition ensures that the cell’s addresses were not changed by the operation call and the last postcondition ensures that the content of the non-addressed cells is not changed. The model finder cannot *guess* the developer’s intention. To ensure that non-related attributes are not changed frame constraints need to be added. Either automatic tools are used that assist the developer in finding them [10] or they are provided manually:

```

post: processor.pc = processor.pc@pre
post: dataout = dataout@pre
post: address = address@pre

```

When evaluating OCL expressions in the context of a system state σ one needs to consider assignments to variables that appear in the OCL expressions. For this purpose, let Var_t be the set of variables of type t , then $\beta : \text{Var}_t \rightarrow I(t)$ is a variable assignment. A context for the evaluation of an OCL expression is given by an environment $\tau = (\sigma, \beta)$. Let ‘Env’ be the set of environments $\tau = (\sigma, \beta)$, then the semantics of an expression $e \in \text{Expr}_t$ is provided by a mapping $I[[e]] : \text{Env} \rightarrow I(t)$.

In this paper we make heavy use of the notation that has been introduced in [11] to formalise UML class diagrams and OCL expressions. The meaning of the notation should be comprehensible from the context, however, for more precise definitions the reader is referred to [11].

2.2 Model Finding

Model finding describes the problem of finding a system state σ to a given model \mathcal{M} with invariants \mathcal{I} such that

$$I[[\mathcal{I}]](\sigma) \stackrel{\text{def}}{=} \bigwedge_{c \in \text{CLASS}} \bigwedge_{e \in \mathcal{I}(c)} \bigwedge_{\underline{c} \in \sigma_{\text{CLASS}}(c)} I[[e]]((\sigma, \text{self} \mapsto \underline{c})) = \text{true} , \quad (7)$$

i.e., all invariants hold for all objects in the system state. Such a system state σ is called *valid* and witnesses the consistency of \mathcal{M} . Usually the size of σ_{CLASS} is predefined such that the model finding problem becomes decidable [2]. This assumption is reasonable, since often the size bounds of the model are known in advance. Similar restrictions are assumed for associations, but not discussed in this paper. We refer to this problem also as *structural* model finding since only one system state is considered. Different implementations for the model finding problem have been proposed in the past [7, 14, 3, 9]. The problem can be extended to consider the class diagram’s operations which is described in the following. For this purpose, some additional notation to formalise operation calls is required.

An operation call $\underline{\omega} = (\underline{c}, \omega)$ is a tuple consisting of an operation $\omega \in \text{OP}_c$ and an object $\underline{c} \in \sigma_{\text{CLASS}}(c)$. How parameters are bound to operation calls is described later in Sect. 3. For interpreting an operation call, both a pre-state σ_{pre} and a post-state and σ_{post} need to be considered. Consequently, also

two environments $\tau_{\text{pre}} = (\sigma_{\text{pre}}, \beta_{\text{pre}})$ and $\tau_{\text{post}} = (\sigma_{\text{post}}, \beta_{\text{post}})$ are required to formalise the semantics of a postcondition. Then, the interpretation of an operation call $\underline{\omega} = (\underline{c}, \omega)$ is

$$I[\underline{\omega}](\sigma, \sigma') \stackrel{\text{def}}{=} \bigwedge_{e \in \triangleleft(\omega)} I[e](\tau_{\text{pre}}) \bigwedge_{e \in \triangleright(\omega)} I[e](\tau_{\text{pre}}, \tau_{\text{post}}) , \quad (8)$$

with $\{\mathbf{self} \mapsto \underline{c}\} \in \beta_{\text{pre}}$ and $\{\mathbf{self} \mapsto \underline{c}'\} \in \beta_{\text{post}}$ where \underline{c}' refers to \underline{c} in the post-state.

Given a Boolean expression e_{task} , the *behavioural* model finding problem [13] asks to find a set of system states $\sigma_1, \dots, \sigma_T$ and a set of operation calls $\underline{\omega}_1, \dots, \underline{\omega}_{T-1}$ such that

$$\bigwedge_{t=1}^T I[\mathcal{Z}](\sigma_t) \wedge \bigwedge_{t=1}^{T-1} I[\underline{\omega}_t](\sigma_t, \sigma_{t+1}) \wedge e_{\text{task}} = \text{true} . \quad (9)$$

Also for this problem, T is usually predefined, since otherwise the problem is undecidable. This assumption is reasonable, since one is interested in a short sequence diagram. The common practice is to increase T in an iterative manner until a solution is found. This is similar to reachability analysis with bounded model checking [4]. Furthermore, the initial state σ_1 is typically preassigned by the developer to exclude false positives, i.e., system states that cannot be reached. Verification tasks in behavioural model checking can, e.g., be the check for a deadlock, i.e., a final system state in which no operation can be called since no preconditions can be asserted, or it can be checked whether an operation is executable by preassigning one of the operation call variables. Besides these, a variety of other reachability tasks can be formulated. Implementations for behavioural model finders have been realised, e.g., in [5, 13].

3 Operation Coverage

In this section, two coverage criteria are defined. Since coverage is witnessed and increased in terms of sequence diagrams, we first need to formalise them analogously to the definitions of the previous section. In this paper, we only consider very simple sequence diagrams with no nested operation calls and no control structures. Consequently, these can also be represented by a sequence of operations.

Definition 6 (Operation sequence). *Given a class diagram \mathcal{M} and a sequence of system states $\sigma_i(\mathcal{M}) = (\sigma_{\text{CLASS},1}, \sigma_{\text{ATT},1})$ for $1 \leq i \leq n$, an operation sequence is an ordered set*

$$S = \langle r_1 \leftarrow \underline{\omega}_1(\mathbf{p}_1), r_2 \leftarrow \underline{\omega}_2(\mathbf{p}_2), \dots, r_n \leftarrow \underline{\omega}_n(\mathbf{p}_n) \rangle . \quad (10)$$

The i^{th} operation call in the sequence S is of the form $r \leftarrow \underline{\omega}(\mathbf{p})$ with $\underline{\omega} = (\underline{c}, \omega)$ stating that an operation $\omega : t_c \times t_1 \times \dots \times t_k \rightarrow t \in \text{OP}_c$ is called on an

object $\underline{c} \in \sigma_{\text{CLASS},i}(c)$ of class c with parameters $\mathbf{p} = (p_1, \dots, p_k)$ with $p_i \in t_i$ and returns a value $r \in t$. We define

$$\text{ops}(S) \stackrel{\text{def}}{=} \{\omega_1, \dots, \omega_n\} . \quad (11)$$

An operation sequence may not necessarily be executable since invariants and operation specifications could prevent the OCL expressions from being asserted.

Definition 7 (Validity of operation sequences). *Operation sequences are executed with respect to some given valid initial system state σ_1 . An operation sequence is called valid if, and only if there exist valid system states $\sigma_2, \dots, \sigma_{n+1}$ such that*

$$\bigwedge_{i=1}^n I[\llbracket \omega_i \rrbracket](\sigma_i, \sigma_{i+1}) = \text{true} . \quad (12)$$

Example 5. Figure 3 shows a valid system state for the running example written in the USE syntax. First, the initial state is prepared which includes object creation and linking. The command `!openter` initiates the operation call and also the preconditions are evaluated. Afterwards, the post-state is prepared before the operation call is finished using `!opexit`, which also evaluates the operation's postcondition.

3.1 Operation call coverage

Now that operation sequences are formally defined, we can formalise the coverage metrics. The first coverage metric, called *operation call coverage*, checks how many of the model's operations have been called in a given set of operation sequences.

Definition 8 (Operation call coverage). *Given a class diagram \mathcal{M} and valid operation sequences S_1, \dots, S_n , the operation call coverage is defined as*

$$\sum_{i=1}^n \frac{|\text{ops}(S_i)|}{|\text{ops}(\mathcal{M})|} . \quad (13)$$

3.2 Subexpression coverage

The second coverage metric is based on an expression's subexpressions and should first be illustrated by means of an example.

Example 6. The operation *process* from the class *Processor* has among other the following two postconditions:

```
post: pc@pre < 9 implies pc = pc@pre + 1
post: pc@pre = 9 implies pc = 0
```

They ensure that the program counter *pc* overflows to 0 if its value is 9 before the operation is called.


```

!create Controller0: Controller
!create Cell0: Cell
!create Cell1: Cell
!create Cell2: Cell
!create Cell3: Cell
!create Processor0: Processor
!set Controller0.address := 13
!set Controller0.dataout := 1
!set Cell0.address := 0
!set Cell0.content := 0
!set Cell1.address := 1
!set Cell1.content := 0
!set Cell2.address := 2
!set Cell2.content := 0
!set Cell3.address := 3
!set Cell3.content := 0
!set Processor0.pc := 0
!set Processor0.instruction := 4
!insert (Controller0, Cell0) into HasCells
!insert (Controller0, Cell1) into HasCells
!insert (Controller0, Cell2) into HasCells
!insert (Controller0, Cell3) into HasCells
!insert (Processor0, Controller0) into HasProgramMemory

!openter Processor0 process()
!set Processor0.pc := 1
!set Processor0.instruction := Undefined
!opexit

!openter Processor0 fetch()
!set Processor0.instruction := 1
!set Controller0.dataout := Undefined
!opexit

```

σ_1 (Initial state)

σ_2

σ_3

Fig. 3. Valid operation sequence

An implication expression always evaluates to true if the antecedent is false. Hence, one is interested in finding operation sequences that assert many subexpressions. The same argument holds for expressions of the form e_1 or e_2 . Even expressions of the form e_1 and e_2 need to be considered although in this case, both e_1 and e_2 must have been true in order to assert the overall conjunction. However, the expression can be more complicated if nested expressions are used as, e.g., in $(e_1$ or $e_2)$ and $(e_3$ or $e_4)$. Also here, it is desired that all subexpressions have been asserted once in some sequence diagram.

For this purpose, we define a function ‘sub’ that returns all Boolean subexpressions of a given expression $e \in \text{Expr}$:

$$\text{sub}(e) \stackrel{\text{def}}{=} \begin{cases} \{e\} \cup \bigcup \{\text{sub}(e') \mid e' \text{ is subexpression of } e\} & \text{if } e \in \text{Expr}_{\text{Boolean}}, \\ \bigcup \{\text{sub}(e') \mid e' \text{ is subexpression of } e\} & \text{otherwise .} \end{cases} \quad (14)$$

Before we define the second coverage metric, we define the evaluation of a Boolean expression $e \in \text{Expr}_{\text{Boolean}}$ in the context of a valid operation sequence $S = \langle \underline{\omega}_1 = (\underline{c}_1, \omega_1), \dots, \underline{\omega}_n = (\underline{c}_n, \omega_n) \rangle$ as defined in (10) (For brevity we omitted parameters and return values from the operation sequence). The operation sequence implies $n + 1$ valid system states $\sigma_1, \dots, \sigma_{n+1}$ as defined in (12). The evaluation of e depends on whether it is part of an invariant, a precondition, or a postcondition. If e is part of an invariant, it is checked whether e evaluates to true in some of these system states for some object, i.e.,

$$I_{\text{inv}}[[e]](S) \stackrel{\text{def}}{=} \bigvee_{i=1}^n \bigvee_{\underline{c} \in \sigma_{\text{CLASS}}(c)} I[[e]]((\sigma_i, \mathbf{self} \mapsto \underline{c})) . \quad (15)$$

If e is part of a precondition of some operation ω , it is checked whether it evaluates to true if the operation has been called, i.e.,

$$I_{\text{pre}}[[e]](S) \stackrel{\text{def}}{=} \bigvee_{i=1}^n (\omega_i \equiv \omega) \wedge I[[e]]((\sigma_i, \mathbf{self} \mapsto \underline{c}_i)) . \quad (16)$$

Finally, if e is part of a postcondition of some operation ω , it is checked whether it evaluates to true in the state after the operation has been called, i.e.,

$$I_{\text{post}}[[e]](S) \stackrel{\text{def}}{=} \bigvee_{i=1}^n (\omega_i \equiv \omega) \wedge I[[e]]((\sigma_i, \mathbf{self} \mapsto \underline{c}_i), (\sigma_{i+1}, \mathbf{self} \mapsto \underline{c}'_i)) , \quad (17)$$

where \underline{c}'_i refers to \underline{c}_i in state σ_{i+1} .

We also define a partition over all subexpressions in a model based on their context, i.e., whether the expression is part of an invariant or of an operation specification:

$$\begin{aligned} \text{sub}_{\text{inv}} &\stackrel{\text{def}}{=} \bigcup_{e \in \mathcal{I}} \text{sub}(e), \\ \text{sub}_{\text{pre}} &\stackrel{\text{def}}{=} \bigcup_{\omega \in \text{ops}(\mathcal{M})} \bigcup_{e \in \triangleleft(\omega)} \text{sub}(e), \text{ and} \\ \text{sub}_{\text{post}} &\stackrel{\text{def}}{=} \bigcup_{\omega \in \text{ops}(\mathcal{M})} \bigcup_{e \in \triangleright(\omega)} \text{sub}(e) \end{aligned}$$

Definition 9 (Subexpression coverage). *Given a class diagram \mathcal{M} and valid operation sequences S_1, \dots, S_n , the subexpression coverage is defined as*

$$\frac{\left| \bigcup_{x \in \{\text{inv}, \text{pre}, \text{post}\}} \{e \in \text{sub}_x \mid \exists S_i : I_x[[e]](S_i)\} \right|}{|\text{sub}_{\text{inv}} \cup \text{sub}_{\text{pre}} \cup \text{sub}_{\text{post}}|} . \quad (18)$$

The formal definition for subexpression coverage is a bit tedious, however, the intuitive idea can readily be stated in an informal manner. We take all subexpressions from the model’s invariants and operation specifications. Then for each of them we check whether they evaluate to true in some intermediate state of the given operation sequences. The number of such subexpressions is divided by the total amount of subexpressions. It is important to ensure that a subexpression of a pre- or postcondition is only checked for a positive evaluation if the respective operation has been called.

Example 7. The operation sequence in Fig. 3 leads to an operation call coverage of 0.4 since 2 out of 5 operations have been called. Furthermore, the subexpression coverage is approximately 0.3 as 14 out of 46 subexpressions are asserted. For instance, the second postcondition of Example 6 is not covered by the operation sequence in Fig. 3 since the program counter pc is initially set to 0. Consequently, the subexpression $pc@pre = 9$ evaluates to false.

4 Algorithm to Automatically Enhance Coverage

Now that the coverage criteria have been defined, in this section we are proposing methods that aim at automatically increasing the coverage by generating new sequence diagrams. Since model finders are used for this purpose, always the whole solution space is considered. Consequently, the algorithm either yields a sequence diagram that indeed increases the coverage or finds that some operation is not executable or some subexpression cannot be asserted by any sequence diagram. Hence, the algorithm is able to determine “dead” model code (analogously to dead code in programs that can never be executed).

The model finder is utilised by choosing an appropriate expression for e_{task} as described in (9). To increase operation call coverage the model finder is used to find an operation sequence that contains an operation ω that has not been executed thus far. In order to constrain the model finder to call an operation in an operation sequence one needs to assign

$$e_{\text{task}} = \bigvee_{t=1}^{T-1} (\omega_t \equiv \omega) . \quad (19)$$

When increasing subexpression coverage a task needs to be formalised for each subexpression e that is not covered yet. Again, the description of the task expression depends on the origin of the subexpression. If e is part of an invariant of class c , we assign

$$e_{\text{task}} = \bigvee_{t=1}^T \bigvee_{c \in \sigma_{\text{CLASS}}(c)} I[[e]]((\sigma_t, \mathbf{self} \mapsto \underline{c})) . \quad (20)$$

For the case if e is part of an operation specification, we could also find a suitable task expression, however, instead we are making use of a small trick. We

are using the same task expression as in (19) but additionally add e as pre- or postcondition to the considered model. Since the task forces the operation to be called also e must evaluate to true.

It can easily be seen that many sequence diagrams may need to be generated in order to obtain full coverage if initially many operations and subexpressions are uncovered. The first measure to avoid this is to recompute the coverage metrics after each generated sequence diagram, since other uncovered elements may be covered by the newly generated sequence diagram. Furthermore, one can also try to cover multiple uncovered elements at once by combing the task expressions that have been introduced above. As an example one can try to find a sequence diagram in which three uncovered operations ω_1 , ω_2 , and ω_3 are called by assigning

$$e_{\text{task}} = \bigvee_{t=1}^{T-1} (\omega_t \equiv \omega_1) \wedge \bigvee_{t=1}^{T-1} (\omega_t \equiv \omega_2) \wedge \bigvee_{t=1}^{T-1} (\omega_t \equiv \omega_3) .$$

However, this approach needs to be applied with care since it may lead to false negatives, since operations may be executable independently but not in combination. A strategy can be to first try to generate sequence diagrams that cover a lot of operations and then decrease the number if no more sequence diagrams can be found.

Alternatively, one can make use of Boolean *select variables* s_1, \dots, s_ℓ for each uncovered operation $\omega_1, \dots, \omega_\ell$. Then these can be considered at once in a single task expression and let the model finder decide which ones to use in the sequence diagram and which ones not:

$$e_{\text{task}} = \bigwedge_{i=1}^{\ell} \left(s_i \Rightarrow \bigvee_{t=1}^{T-1} (\omega_t \equiv \omega_i) \right) \wedge \sum_{i=1}^{\ell} s_i \geq k . \quad (21)$$

If a select variable s_i is assigned 1, then the operation ω_i must be called in the sequence diagram. Since e_{task} can easily be satisfied by assigning all select variables to 0, a cardinality constraint ensures that at least k select variables must be assigned 1 and hence at least k operations must be called in the operation sequence. The value for k can be initially set high and then decreased successively if e_{task} cannot be satisfied.

5 Tool Support

We implemented the proposed algorithm as a plugin of the UML specification tool USE [6]. Consequently, models in the form of class diagrams as well as sequence diagrams are to be provided in the USE format (`.use` and `.cmd` to specify class diagrams and operation sequences, respectively). We have used the SMT-based behavioural model finder that has been proposed in [13].

The features realised in the plugin are the following:⁴

⁴ A USE plugin for computing and displaying coverage information can be downloaded from www.informatik.uni-bremen.de/agra/files/coverage-plugin.zip

Coverage	
Initial Coverage	Maximum Coverage
read	Not Covered
pre3: self.address.isDefined	Not Covered
post8: self.cells->forall(c : Cell ((c.address = c.address@pre) and (c.content = c.content@pre)))	Not Covered
post9: self.cells->one(c : Cell ((c.address = self.address@pre) and (c.content = self.dataout)))	Not Covered
post10: self.address.isUndefined	Not Covered
post11: (self.processor.instruction = self.processor.instruction@pre)	Not Covered
post12: (self.processor.pc = self.processor.pc@pre)	Not Covered
write	Not Covered
pre1: (self.address < 10)	Not Covered
pre2: (content < 4)	Not Covered
post1: self.cells->one(c : Cell ((c.address = self.address) and (c.content = content)))	Not Covered
post2: self.cells->forall(c : Cell (c.address = c.address@pre))	Not Covered
post3: self.cells->forall(c : Cell ((c.address <= self.address) implies (c.content = c.content@pre)))	Not Covered
post4: (self.processor.instruction = self.processor.instruction@pre)	Not Covered
post5: (self.processor.pc = self.processor.pc@pre)	Not Covered
post6: (self.dataout = self.dataout@pre)	Not Covered
post7: (self.address = self.address@pre)	Not Covered
fetch	Not Covered
pre5: self.programMemory.dataout.isDefined	Not Covered
post18: (self.instruction = self.programMemory.dataout@pre)	Not Covered
post19: (self.pc = self.pc@pre)	Not Covered
post20: self.programMemory.cells->forall(c : Cell ((c.address = c.address@pre) and (c.content = c.content@pre)))	Not Covered
post21: (self.programMemory.address = self.programMemory.address@pre)	Not Covered
post22: self.programMemory.dataout.isUndefined	Not Covered
prepareMemory	Not Covered
pre4: self.programMemory.address.isUndefined	Not Covered
post13: (self.programMemory.address = self.pc)	Not Covered
post14: (self.instruction = self.instruction@pre)	Not Covered
post15: (self.pc = self.pc@pre)	Not Covered
post16: (self.programMemory.dataout = self.programMemory.dataout@pre)	Not Covered
post17: self.programMemory.cells->forall(c : Cell ((c.address = c.address@pre) and (c.content = c.content@pre)))	Not Covered
process	Covered
pre6: self.instruction.isDefined	Covered
post23: ((self.pc@pre < 9) implies (self.pc = (self.pc@pre + 1)))	Maybe Covered
post24: ((self.pc@pre = 9) implies (self.pc = 0))	Maybe Covered
post25: self.programMemory.cells->forall(c : Cell ((c.address = c.address@pre) and (c.content = c.content@pre)))	Covered
post26: self.instruction.isUndefined	Covered
post27: (self.programMemory.address = self.programMemory.address@pre)	Covered
post28: (self.programMemory.dataout = self.programMemory.dataout@pre)	Covered
Initial: Covered 6/39 Elements	15%
Maximum: Covered 14/39 Elements	36%

Fig. 4. Initial coverage

1. computation of the initial coverage of operations and constraints based on the provided sequence diagram(s) (Fig. 4)
2. computation of the maximal possible coverage of operations and constraints using a model finder in the background (Fig. 5)
3. display of the results

Both types of coverage are computed upon start of the plugin in the background if a model is provided. Without given sequence diagrams, the initial status of each operation and each constraint is displayed as *not covered*. If coverage of the respective operation or constraint has been reached, the mark is changed to *covered*. The remaining constraints are marked as *maybe covered* in the initial computation and as *partially covered* in the final state.

A constraint is *maybe covered* if the respective operation has been executed but the constraint contains subexpressions which might not hold, e.g., a part of a disjunction where the whole constraint can evaluate to true while a single subexpression evaluates to false. A constraint is eventually *partially covered* when

Coverage	
Initial Coverage	Maximum Coverage
read	Not Covered
pre3: self.address.isDefined	Not Covered
post8: self.cells->forall(c : Cell ((c.address = c.address@pre) and (c.content = c.content@pre)))	Not Covered
post9: self.cells->one(c : Cell ((c.address = self.address@pre) and (c.content = self.dataout)))	Not Covered
post10: self.address.isUndefined	Not Covered
post11: (self.processor.instruction = self.processor.instruction@pre)	Not Covered
post12: (self.processor.pc = self.processor.pc@pre)	Not Covered
write	Not Covered
pre1: (self.address < 10)	Not Covered
pre2: (content < 4)	Not Covered
post1: self.cells->one(c : Cell ((c.address = self.address) and (c.content = content)))	Not Covered
post2: self.cells->forall(c : Cell (c.address = c.address@pre))	Not Covered
post3: self.cells->forall(c : Cell ((c.address <= self.address) implies (c.content = c.content@pre)))	Not Covered
post4: (self.processor.instruction = self.processor.instruction@pre)	Not Covered
post5: (self.processor.pc = self.processor.pc@pre)	Not Covered
post6: (self.dataout = self.dataout@pre)	Not Covered
post7: (self.address = self.address@pre)	Not Covered
fetch	Covered
pre5: self.programMemory.dataout.isDefined	Covered
post18: (self.instruction = self.programMemory.dataout@pre)	Covered
post19: (self.pc = self.pc@pre)	Covered
post20: self.programMemory.cells->forall(c : Cell ((c.address = c.address@pre) and (c.content = c.content@pre)))	Covered
post21: (self.programMemory.address = self.programMemory.address@pre)	Covered
post22: self.programMemory.dataout.isUndefined	Covered
prepareMemory	Not Covered
pre4: self.programMemory.address.isUndefined	Not Covered
post13: (self.programMemory.address = self.pc)	Not Covered
post14: (self.instruction = self.instruction@pre)	Not Covered
post15: (self.pc = self.pc@pre)	Not Covered
post16: (self.programMemory.dataout = self.programMemory.dataout@pre)	Not Covered
post17: self.programMemory.cells->forall(c : Cell ((c.address = c.address@pre) and (c.content = c.content@pre)))	Not Covered
process	Covered
pre6: self.instruction.isDefined	Covered
post23: ((self.pc@pre < 9) implies (self.pc = (self.pc@pre + 1)))	Covered
post24: ((self.pc@pre = 9) implies (self.pc = 0))	Partially Covered
post25: self.programMemory.cells->forall(c : Cell ((c.address = c.address@pre) and (c.content = c.content@pre)))	Covered
post26: self.instruction.isUndefined	Covered
post27: (self.programMemory.address = self.programMemory.address@pre)	Covered
post28: (self.programMemory.dataout = self.programMemory.dataout@pre)	Covered
Initial: Covered 6/39 Elements	15%
Maximum: Covered 14/39 Elements	36%

Fig. 5. Maximised coverage

it was not possible to find an operation sequence such that all subexpressions eventually hold, i.e., one part of the constraint is covered and one part of it is not covered.

During computation of the maximum coverage, sequence diagrams for the not yet covered operations and constraints are produced and printed to the USE shell. The overall results of the coverage enhancement are always displayed at the bottom of the window. Here, the amount of initially and finally covered elements are provided as well as a progress bar depicting the coverage percentage.

5.1 Experimental Evaluation

For an experimental evaluation, the approach has been applied to several models which have been provided with the tool USE or have been written by the authors. Table 1 shows the results of said evaluation. The first column gives the names of the models. In the second and third column, their initial and maximal coverage is

stated. Then, the amount of generated sequences is provided and the last column contains the required run-times.

In all cases except for one, an increase in coverage up to 94–100% could be achieved. Only for the test case *CPU*, the initial coverage of 0% remained unchanged. This means that no operation sequences could be generated and, consequently, none of the constraints were triggered. This scenario may occur due to two reasons: (1) The initial state may be chosen poorly so that no operation’s pre-condition evaluates to true. (2) The post-conditions of the operations may be contradictory, preventing the operation’s execution even if a pre-condition is satisfied. In both cases, the model as well as the initial state have to be revised.

With regard to the run-time, it can be stated that only the test case *Memory* required a slightly longer execution time than the remaining examples. Since this example is by far the largest one in terms of OCL constraints and has a relatively low initial coverage of 15%, a slight increase in run-time was to be expected.

Overall, it was possible to reach high coverage percentages in negligible run-time by generating a maximum of 6 operation call sequences. In two cases, constraints and/or operations could be detected thanks to our approach which could not be covered by sequences starting in the initial state. These constraints/operations would result in dead code, so by uncovering them, the quality of the resulting implementation can be improved.

6 Related Work

In [12] coverage criteria are defined based on sequence diagrams which are extracted by reverse engineering of existing Java source code. Branches in the source code are mapped to guarded messages in the sequence diagram and therefore each sequence diagram defines a set of possible execution paths. Coverage criteria are based on these paths. The authors have not provided methods to automatically increase the coverage criteria.

The authors of [1] propose three test coverage criteria for class diagrams. These criteria specify a certain structure of an object diagram which has to be created by a test case in order to reach full coverage. In two cases, this structure is determined by constructing representative values for association-end multiplicities and attributes. The third criterion considers generalisation relationships. All three criteria do not consider behavioural aspects and only one criterion takes OCL constraints into consideration.

Table 1. Experimental results

Model	Initial	Maximal	#Sequences	Run-time
CPU	0%	0%	0	<0.01s
Traffic	35%	94%	4	<0.01s
Memory	15%	97%	6	0.22s
Car	0%	100%	4	<0.01s
Life	0%	100%	3	<0.01s

Scenario-based design analysis (SUDA) is defined in [16]. The authors introduce snapshot models to transform a class model with operations into a static model of behaviour which can be verified against a sequence of operation calls. Based on this technique an automatic approach is presented in [17]. Given a UML class diagram with OCL operation specifications and invariants and a specification for a desired scenario, a scenario is automatically generated using model finding techniques.

In [8] a technique is presented that allows animation of UML class diagrams with OCL operation specifications and invariants. Given a current state, a post-state is computed that satisfies the class diagram's invariants and underspecified postconditions.

7 Conclusions

We concerned ourselves with the question of how to formalise coverage criteria of class diagrams with respect to a set of sequence diagrams in terms of operation sequences. We have defined two coverage criteria, one which checks how many operations are called and another one which considers whether all subexpressions in the involved OCL constraints evaluate to true. Based on the formalisation of UML class diagrams and OCL expressions introduced in [11] we formalised the coverage criteria to enable their application in formal analysis.

We demonstrated how model finders can be utilised in order to automatically generate new sequence diagrams that increase overall coverage of the class diagram. By using the model finder the developer is also pinpointed to operations and OCL subexpressions that can never be executed or asserted, respectively. Our algorithm has been implemented in the UML specification tool USE.

It is a well-known fact that model finders cannot be applied to arbitrarily large models and face scalability problems as the number of classes and constraints increases. Consequently, the efficiency of our approach for automatically generating sequence diagrams heavily depends on the efficiency of the model finder. For now, we have evaluated the generation approach to class diagrams similar to the running example of the paper. For these, sequence diagrams can be generated within a few seconds. In future work we want to evaluate the scalability of the approach in more detail by comparing different model finders. Alternatively, model finders can be tuned to perform well for these kind of problem. Furthermore, more advanced sequence diagrams, e.g., with nested operations, will enhance the usability of the approach.

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